

POWER CRUNSH X1P INTEL SKL-H SYSTEM DIAGRAM

01

+3V/+5V S5
PG.40
+1.0V/+1.35VSUS
PG.42,41
CPU Core
PG.44~46
DDR3L
PG.41
Charge
PG.39

SODIMM1
Max. 8GB
STD PG.17

SODIMM2
Max. 8GB
STD PG.18

INTEL SkyLake-H
Processor : Daul / Quad Core
Power : 45 (Watt)
Package : BGA1400
Size : 42 x 28 (mm)

NVIDIA N16P-GT
Package 29*29mm
40W
PAGE 19~23

VRAM DDR3 x 8
256M X 16 X 8
900Mhz
PAGE 24~27

Stackup

TOP
GND
IN1
IN2
VCC
IN3
GND
BOT

mSATA/NGFF
PG.35

HDD
PG.37

ODD
PG.37

INTEL PCH Lynx Point
Power : Watt
Package : FCBGA837
Size : 23 x 23 (mm)
PG.9~15

USB3.0 Ports
combo x2+x1
PG.32; PG.35

Webcam
PG.16

Touch Screen
Elan EKTH3915 for 14", 15"
Elan EKTH3918 for 17"
PG.33

LAN (DB)
RTL8111GSH
10/100/1000 PG.32

WLAN BT COMBO
PG.37

Accelerometer
PG.35

Card Reader
RTS5237 (DB)
PG.32

KBC
ITE IT8987E/BX
PG.38

TPM
SLB96S6TT1.2
PAGE 35

KB
PG.34

TP
PG.34

ROM
PG.12

FAN
PG.34

SLG3NB3454
GreenCLK (Ni)
PAGE 35

AUDIO CODEC
ALC 3241
PG.30

Headphone amplifier
TPA6133A2
PAGE 32

Combo Jack
PAGE 30

Subwoofer amplifier
ALC1301 (Reserve)
PAGE 33

Speaker
PAGE 30

Dual Digital MIC
PAGE 30

20150616_1200

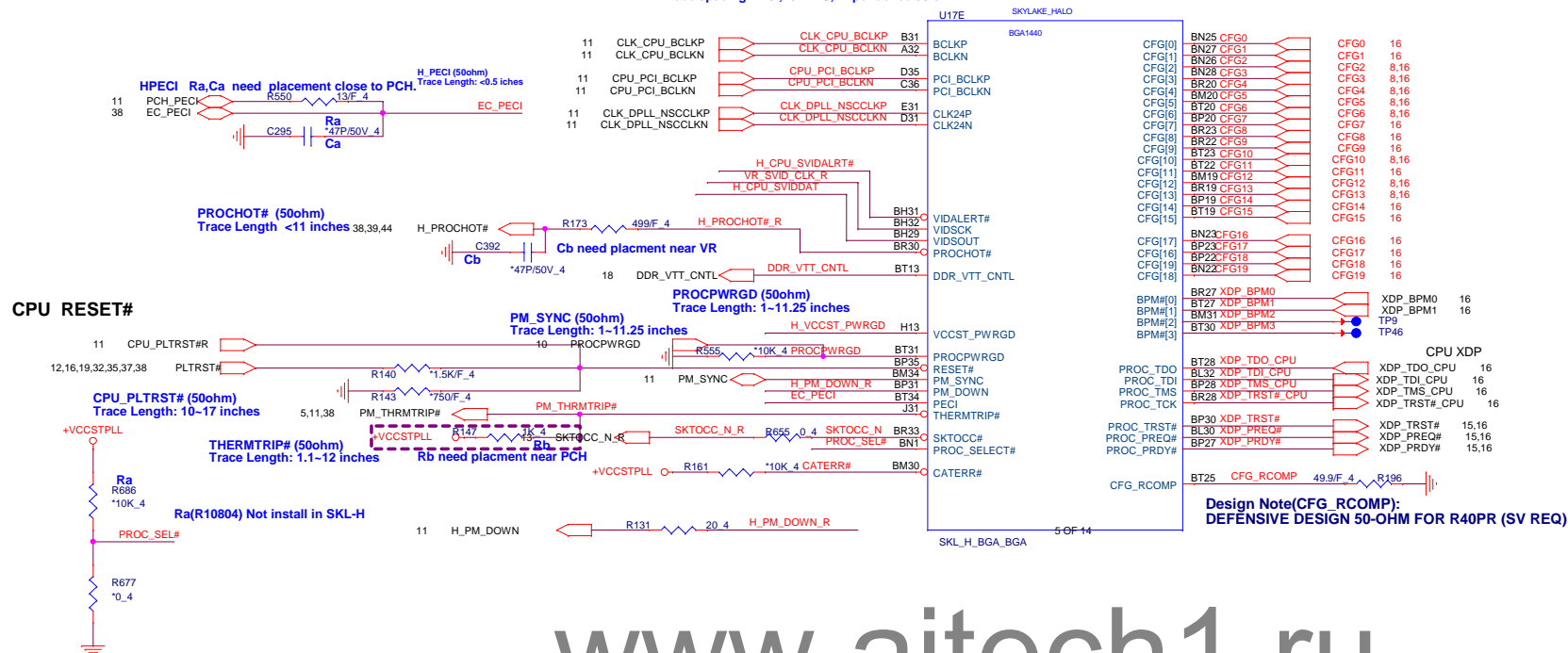


PROJECT : Y19C
Quanta Computer Inc.

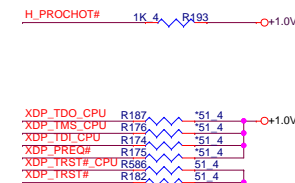
Size Custom Document Number **BLOCK DIAGRAM** Rev 2A
Date: Tuesday, June 16, 2015 Sheet 1 of 51

SKYLAKE Processor (CLK,MISC,JTAG)

Host CLK:
Trace length < 11000 MILS
Trace spacing = 15 ,20 MILS, Impedence 90 ohm



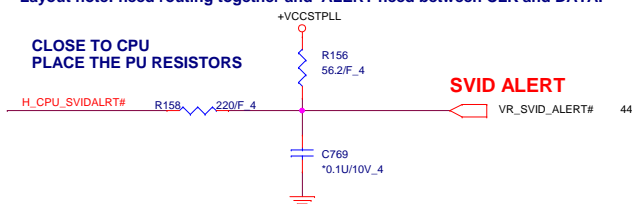
Processor pull-up (CPU)



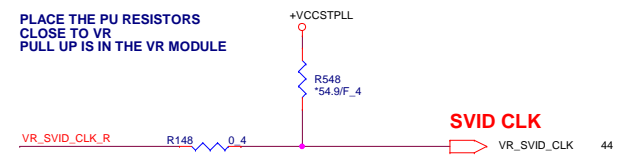
CPU CORE SVID

Layout note: need routing together and ALERT need between CLK and DATA.

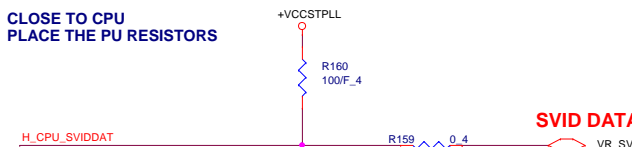
**CLOSE TO CPU
PLACE THE PU RESISTORS**



PLACE THE PU RESISTORS
CLOSE TO VR
PULL UP IS IN THE VR MODULE

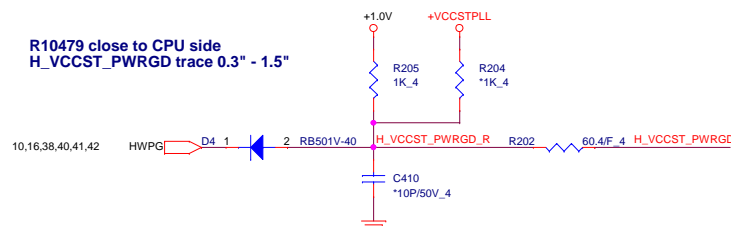


**CLOSE TO CPU
PLACE THE PU RESISTORS**

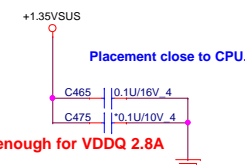


HWPD

R10479 close to CPU side
H VCCST PWRGD trace 0.3" - 1.5"



CPU VDDQ

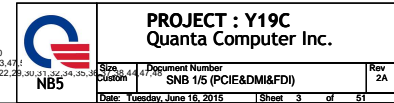
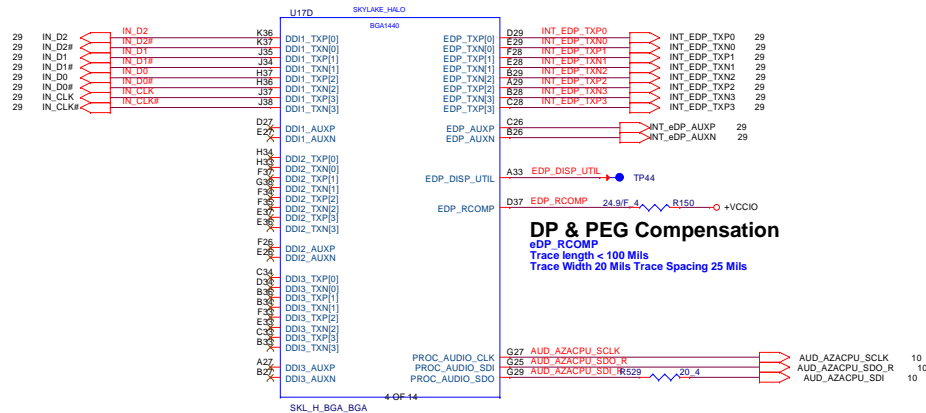


Note: please keep plane is enough for VDDQ 2.8A

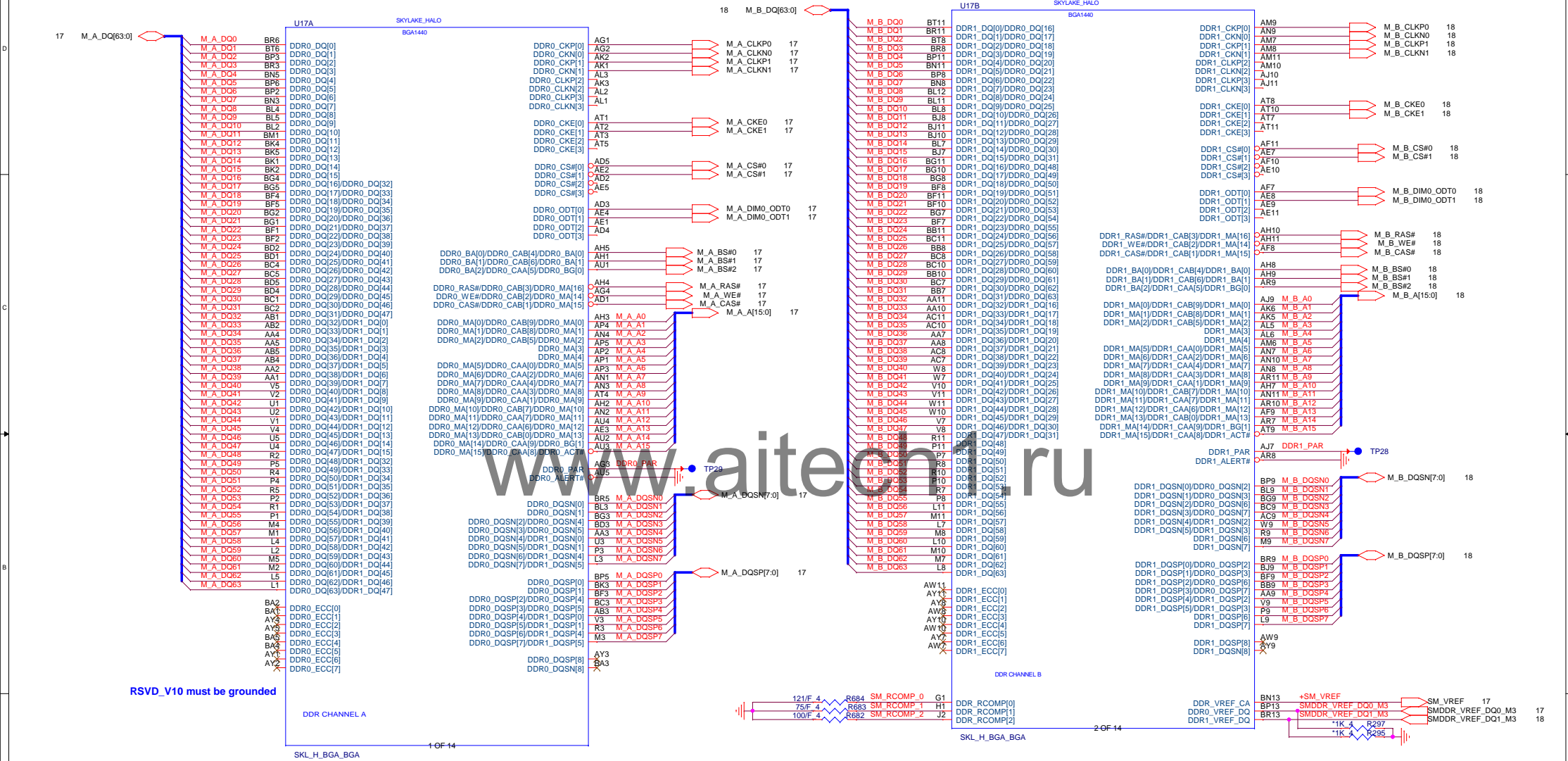


PROJECT : Bellagio
Quanta Computer Inc.

Size Custom	Document Number 02 -- SKYPAKE 1/20(eDP/DDI)	Rev 1A
Date: Tuesday, June 16, 2015	Sheet 2 of	51

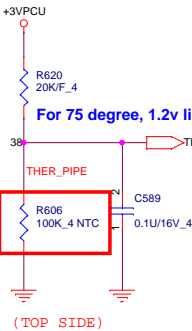
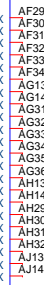


SKYLAKE Processor (DDR3)



4+4e, Support eDRAM Only, GTX 12A

	+VCC _{core}	7,44,45
	+1.35VSUS	2,6,10,17,18,41,43,50

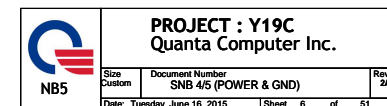


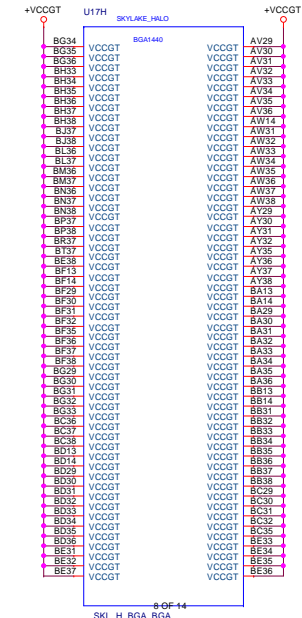
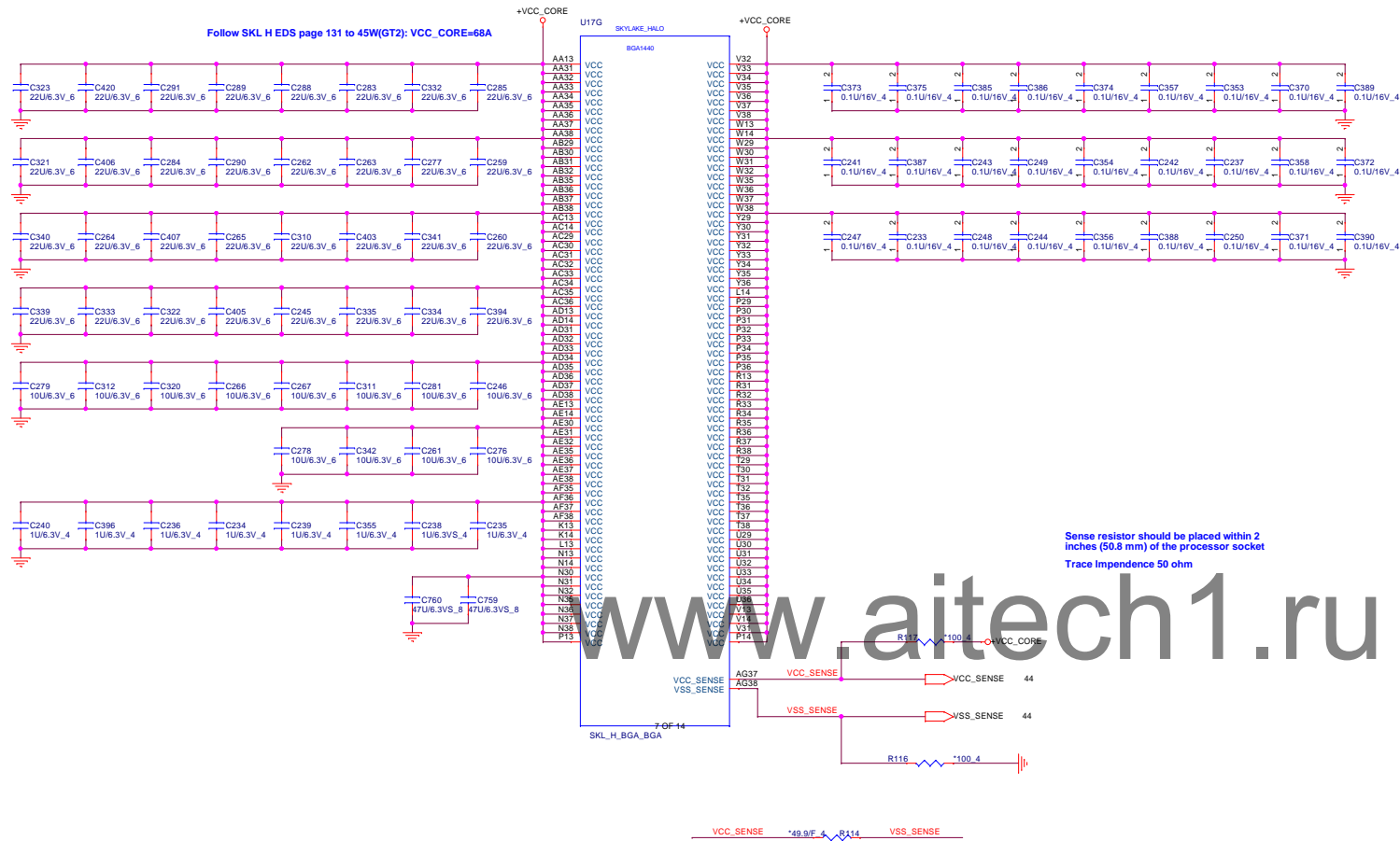
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14 OF 14

10,18,38 MBCLK2 MBCLK2 8 SCLK VCC 1 2 3 4 5 6 7 8 9 10 11 12 13 14 15 16 17 18 19 20 21 22 23 24 25 26 27 28 29 30 31 32 33 34 35 36 37 38 39 40 41 42 43 44 45 46 47 48 49 50 51 52 53 54 55 56 57 58 59 60 61 62 63 64 65 66 67 68 69 70 71 72 73 74 75 76 77 78 79 80 81 82 83 84 85 86 87 88 89 90 91 92 93 94 95 96 97 98 99 100 101 102 103 104 105 106 107 108 109 110 111 112 113 114 115 116 117 118 119 120 121 122 123 124 125 126 127 128 129 130 131 132 133 134 135 136 137 138 139 140 141 142 143 144 145 146 147 148 149 150 151 152 153 154 155 156 157 158 159 160 161 162 163 164 165 166 167 168 169 170 171 172 173 174 175 176 177 178 179 180 181 182 183 184 185 186 187 188 189 190 191 192 193 194 195 196 197 198 199 200 201 202 203 204 205 206 207 208 209 210 211 212 213 214 215 216 217 218 219 220 221 222 223 224 225 226 227 228 229 230 231 232 233 234 235 236 237 238 239 240 241 242 243 244 245 246 247 248 249 250 251 252 253 254 255 256 257 258 259 260 261 262 263 264 265 266 267 268 269 270 271 272 273 274 275 276 277 278 279 280 281 282 283 284 285 286 287 288 289 290 291 292 293 294 295 296 297 298 299 300 301 302 303 304 305 306 307 308 309 310 311 312 313 314 315 316 317 318 319 320 321 322 323 324 325 326 327 328 329 330 331 332 333 334 335 336 337 338 339 340 341 342 343 344 345 346 347 348 349 350 351 352 353 354 355 356 357 358 359 360 361 362 363 364 365 366 367 368 369 370 371 372 373 374 375 376 377 378 379 380 381 382 383 384 385 386 387 388 389 390 391 392 393 394 395 396 397 398 399 400 401 402 403 404 405 406 407 408 409 410 411 412 413 414 415 416 417 418 419 420 421 422 423 424 425 426 427 428 429 430 431 432 433 434 435 436 437 438 439 440 441 442 443 444 445 446 447 448 449 450 451 452 453 454 455 456 457 458 459 460 461 462 463 464 465 466 467 468 469 470 471 472 473 474 475 476 477 478 479 480 481 482 483 484 485 486 487 488 489 490 491 492 493 494 495 496 497 498 499 500 501 502 503 504 505 506 507 508 509 510 511 512 513 514 515 516 517 518 519 520 521 522 523 524 525 526 527 528 529 530 531 532 533 534 535 536 537 538 539 540 541 542 543 544 545 546 547 548 549 550 551 552 553 554 555 556 557 558 559 560 561 562 563 564 565 566 567 568 569 570 571 572 573 574 575 576 577 578 579 580 581 582 583 584 585 586 587 588 589 590 591 592 593 594 595 596 597 598 599 600 601 602 603 604 605 606 607 608 609 610 611 612 613 614 615 616 617 618 619 620 621 622 623 624 625 626 627 628 629 630 631 632 633 634 635 636 637 638 639 640 641 642 643 644 645 646 647 648 649 650 651 652 653 654 655 656 657 658 659 660 661 662 663 664 665 666 667 668 669 670 671 672 673 674 675 676 677 678 679 680 681 682 683 684 685 686 687 688 689 690 691 692 693 694 695 696 697 698 699 700 701 702 703 704 705 706 707 708 709 710 711 712 713 714 715 716 717 718 719 720 721 722 723 724 725 726 727 728 729 730 731 732 733 734 735 736 737 738 739 740 741 742 743 744 745 746 747 748 749 750 751 752 753 754 755 756 757 758 759 760 761 762 763 764 765 766 767 768 769 770 771 772 773 774 775 776 777 778 779 780 781 782 783 784 785 786 787 788 789 790 791 792 793 794 795 796 797 798 799 800 801 802 803 804 805 806 807 808 809 810 811 812 813 814 815 816 817 818 819 820 821 822 823 824 825 826 827 828 829 830 831 832 833 834 835 836 837 838 839 840 841 842 843 844 845 846 847 848 849 850 851 852 853 854 855 856 857 858 859 860 861 862 863 864 865 866 867 868 869 870 871 872 873 874 875 876 877 878 879 880 881 882 883 884 885 886 887 888 889 890 891 892 893 894 895 896 897 898 899 900 901 902 903 904 905 906 907 908 909 910 911 912 913 914 915 916 917 918 919 920 921 922 923 924 925 926 927 928 929 930 931 932 933 934 935 936 937 938 939 940 941 942 943 944 945 946 947 948 949 950 951 952 953 954 955 956 957 958 959 960 961 962 963 964 965 966 967 968 969 970 971 972 973 974 975 976 977 978 979 980 981 982 983 984 985 986 987 988 989 990 991 992 993 99



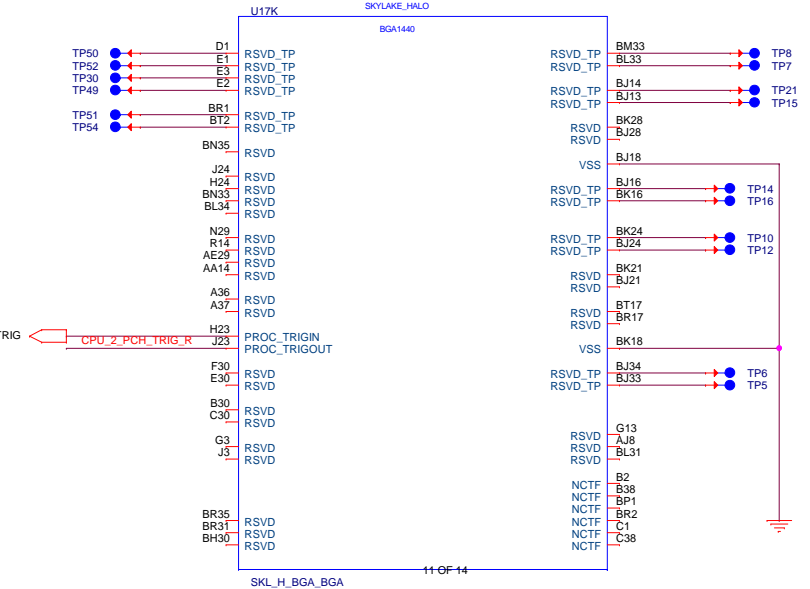
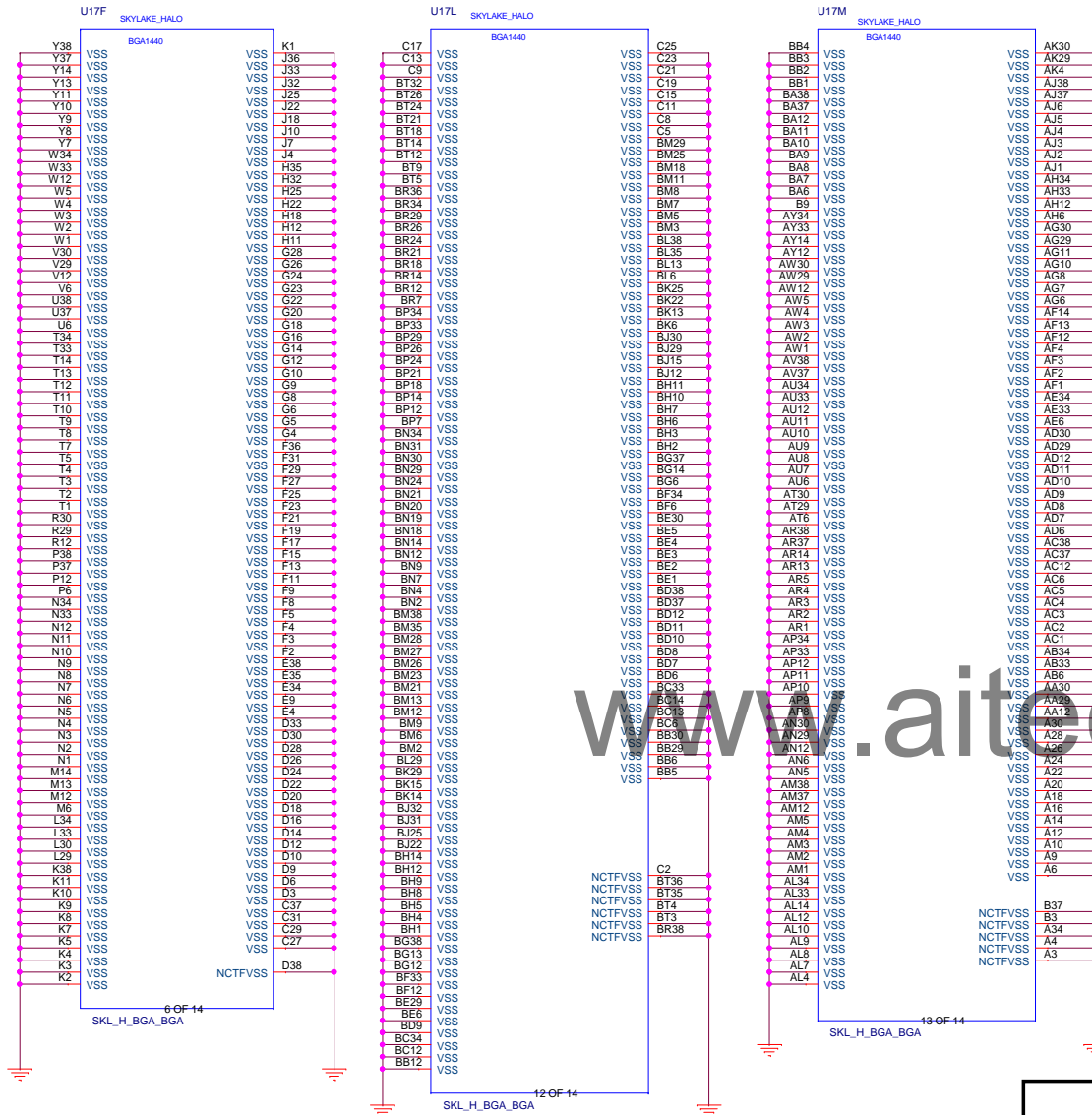


PROJECT : Y19C
Quanta Computer Inc.

Size	Document Number	Rev
Custom	SNB 4/5 (POWER & GND)	2A
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SKL-HProcessor (GND)

SKL-H Processor (RESERVED, CFG)

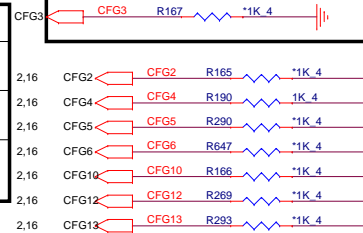


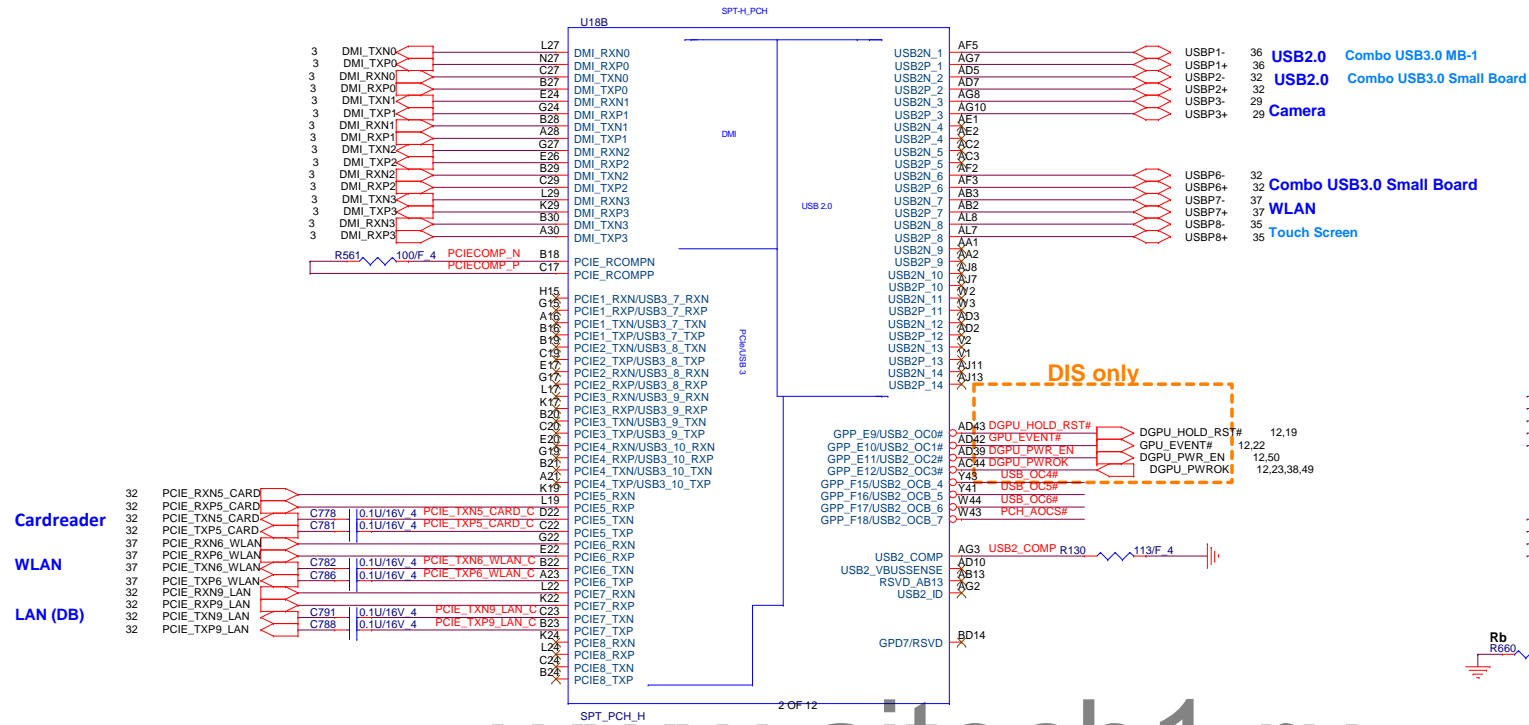
Processor Strapping

The CFG signals have a default value of '1' if not terminated on the board.

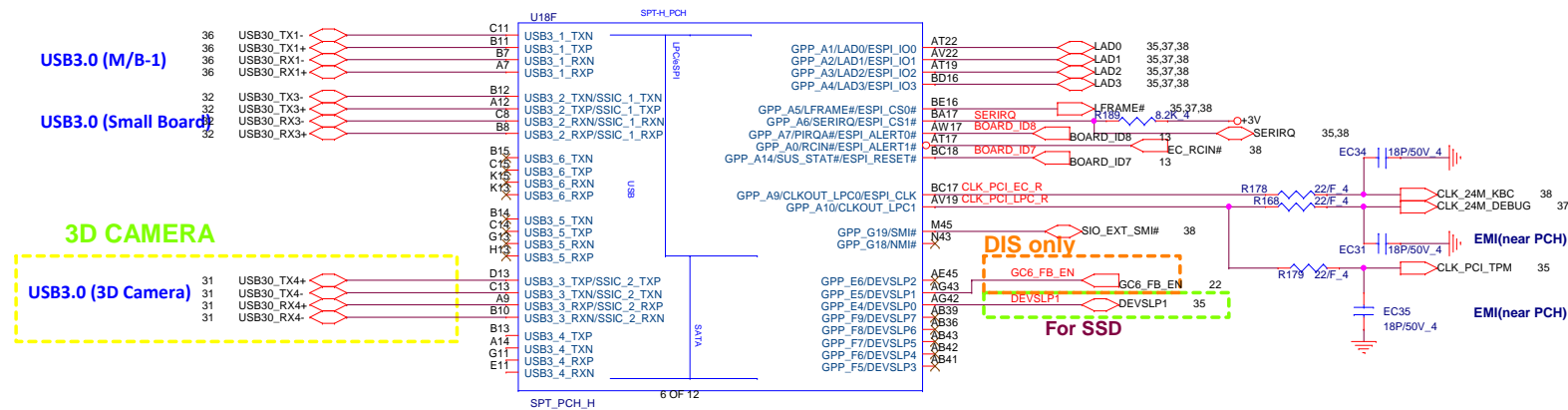
	1	0	2,16

0 Enable; SET DFX ENABLED BIT IN DEBUG
1, Disable;

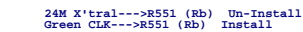
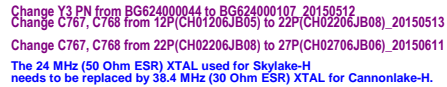


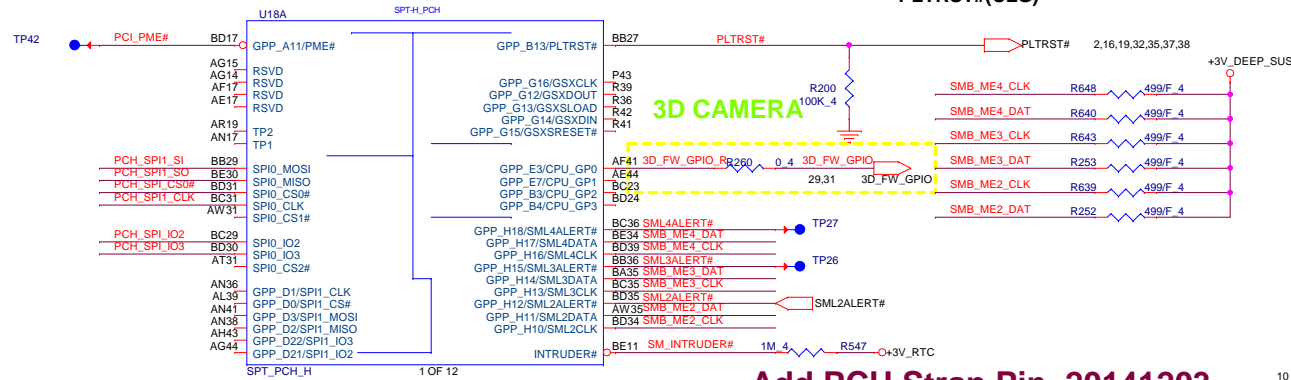


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10,12,13,14,16,18 +3V_DEEP_SUS





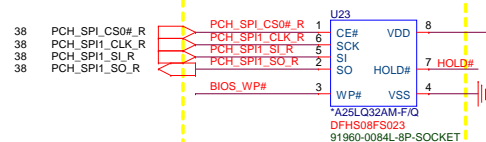
Vender	Size	P/N
EON	8MB	AKE3EZN0Q01 (EN25QH64-104HIP (QE
Winbond	8MB	AKE3EP0N07 (W25Q64FVSSIQ)
GigaDevice	8MB	AKE3EGN0Q01 (GD25B64BSIGR)

PCH SPI ROM(CLG)

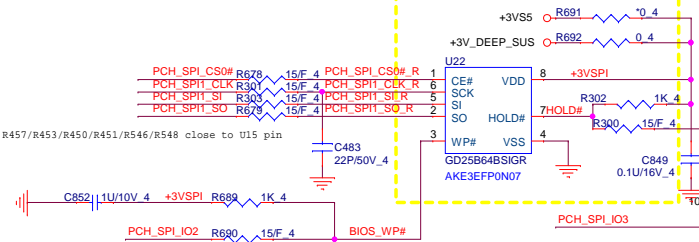
Vender	Size	P/N
EON	8MB	AKE3EZNOQ01 (EN25QH64-104HIP)
Winbond	8MB	AKE3EFP0N07 (W25Q64FVSSIQ)
GigaDevice	8MB	AKE3EGNOQ01 (GD25B64BSIGR)
Socket		DFHS08FS023

Change U23 from I to NI_20150611
Change U22 from NI to I_20150611

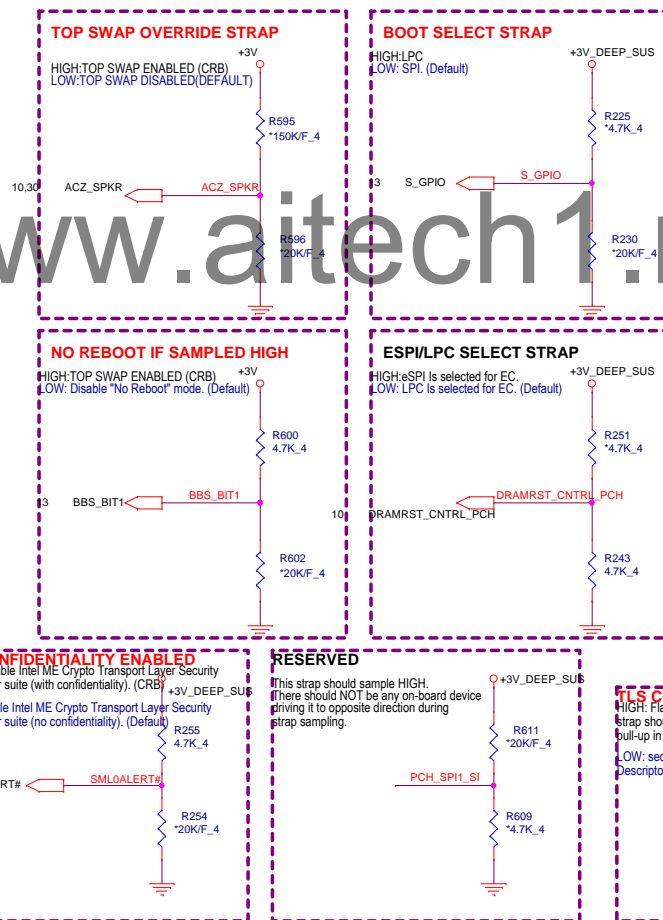
4M SPI ROM Socket



U22&U23 footprint 要重疊
PCH SPI ROM(CLG)

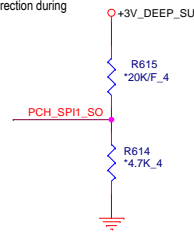


Add PCH Strap Pin_20141203



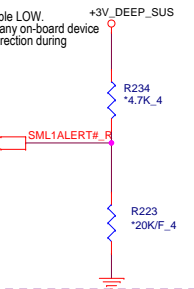
RESERVED

• This strap should sample HIGH.
• There should NOT be any on-board device driving it to opposite direction during strap sampling.



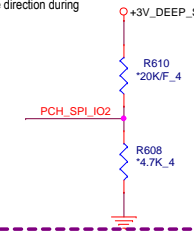
RESERVED

- This strap should sample LOW.
- There should NOT be any on-board driving it to opposite direction during strap sampling.



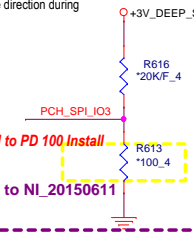
RESERVE

- This strap should sample HIGH
- There should NOT be any on-be
- driving it to opposite direction d
- strap sampling.



RESERVE

- This strap should sample HIGH
- There should NOT be any on-be
- driving it to opposite direction d
- strap sampling.

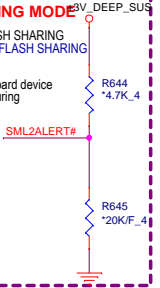


Change R613 from I to NI_20150611

ESPI FLASH SHARING MODE

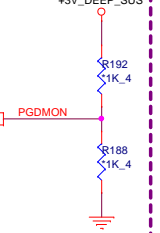
HIGH: SLAVE ATTACHED FLASH SHARING
LOW: 0: MASTER ATTACHED FLASH SHARING

This strap should sample LOW.
There should NOT be any on-board
driving it to opposite direction during
strap sampling.



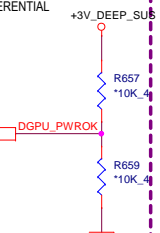
DFX TEST MODE QUALIFIER FOR OTHER DFX STRAP WHEN SAMPLED LOW +3V DFEER SUS

43V_DEEP_303



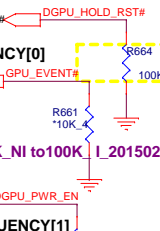
DFX TEST MODE
XTAL INPUT IS SINGLE ENDED IF

SAMPLED LOW ELSE DIFFERENTIAL +3V_DEEP_SUS



RING OSCILLATOR BYPASS

9,19 DGPU_HOLD_RST# DGPU_HOLD_RST#



Change R664 from 10K_NI to 100K_NI_20150204

XTAL INPUT FREQUENCY[1]

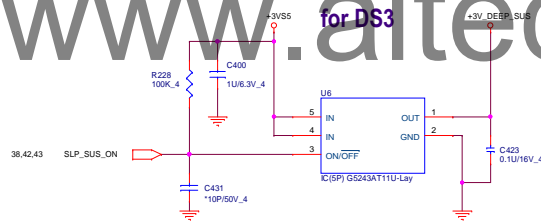
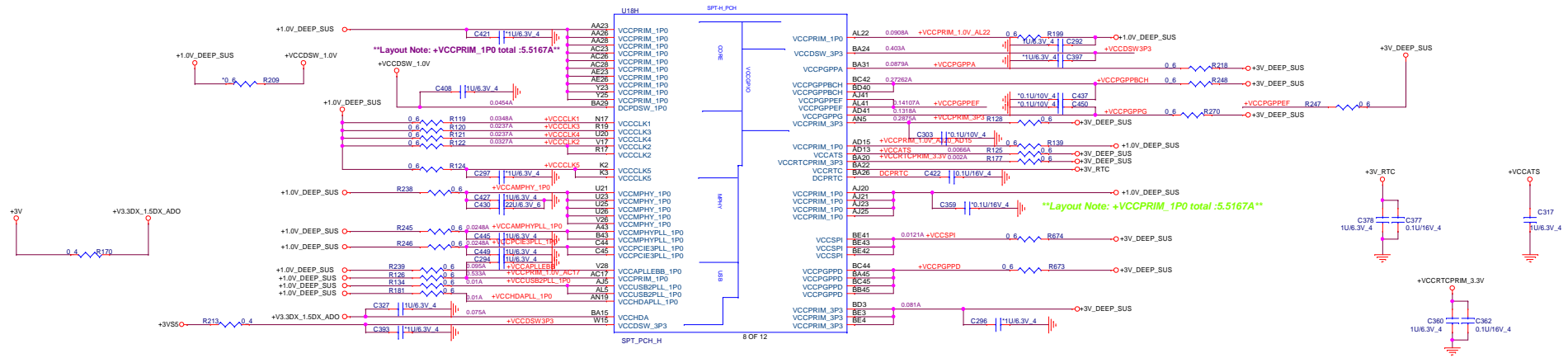
R250
*10K_{min}4




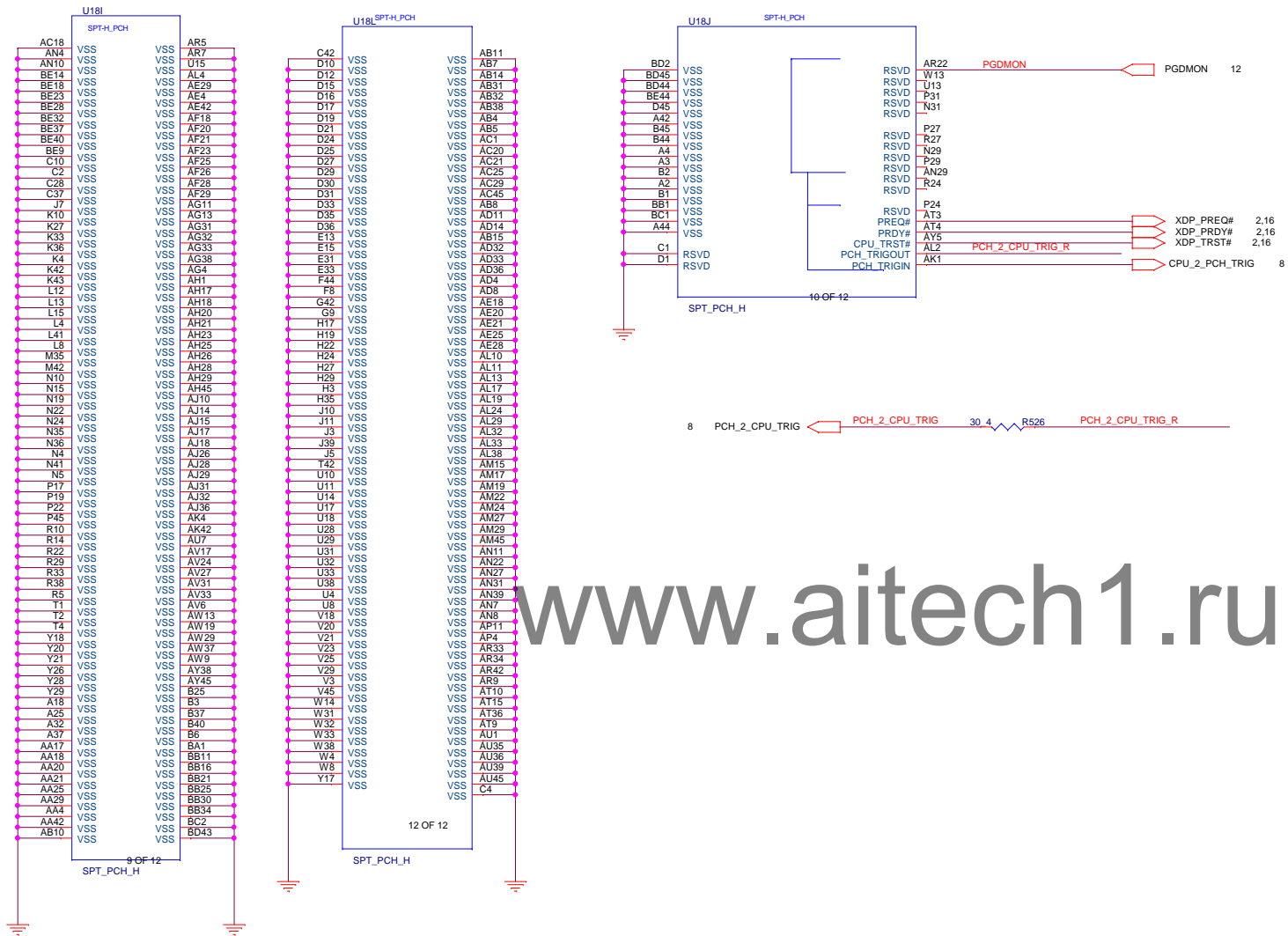
PROJECT : Y19C
Quanta Computer Inc.

Size Custom	Document Number PCH 4/6 (GPIO/MISC)	Rev 2A
Date: Tuesday, June 16, 2015		Sheet 12 of 51

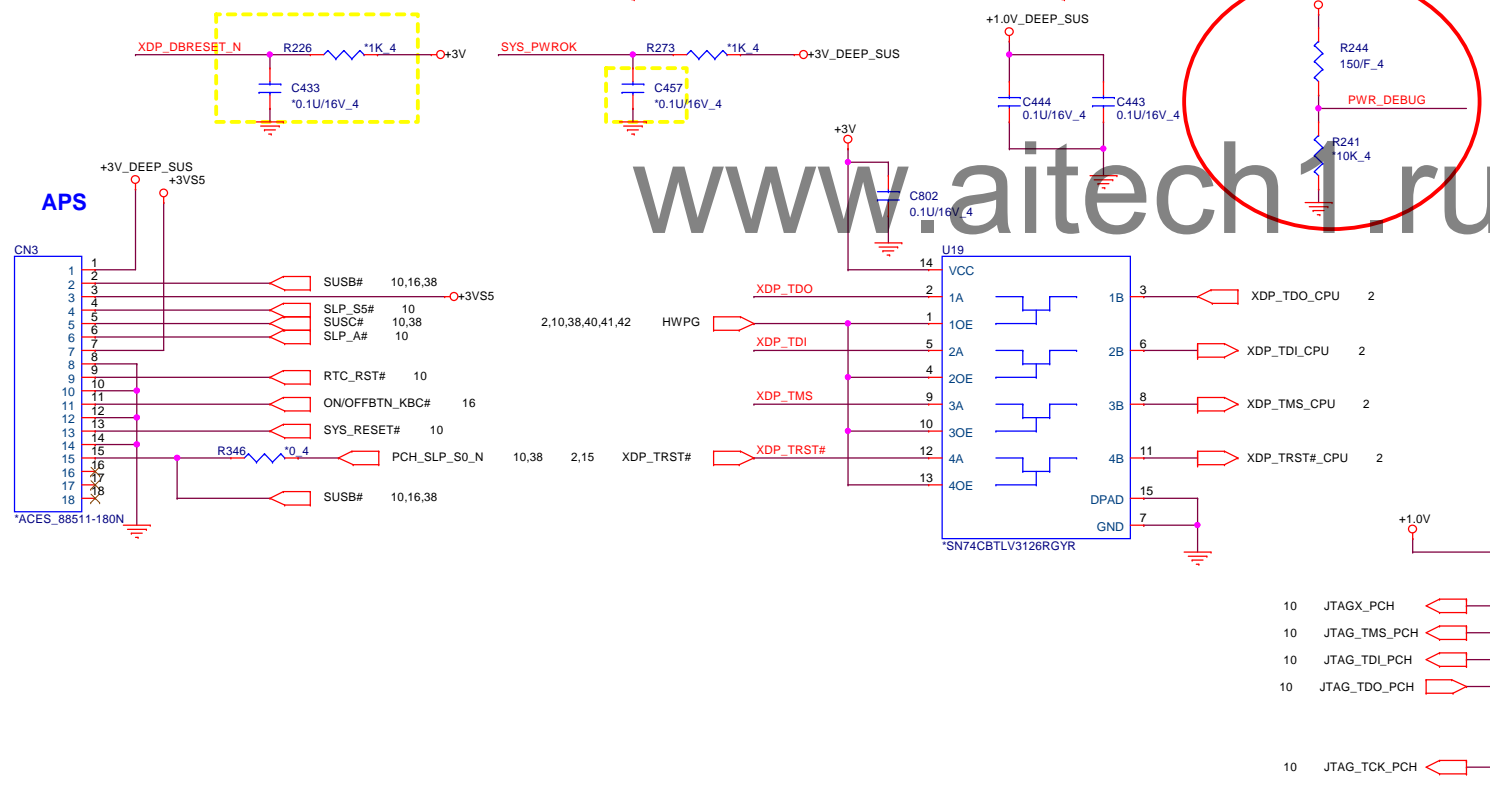
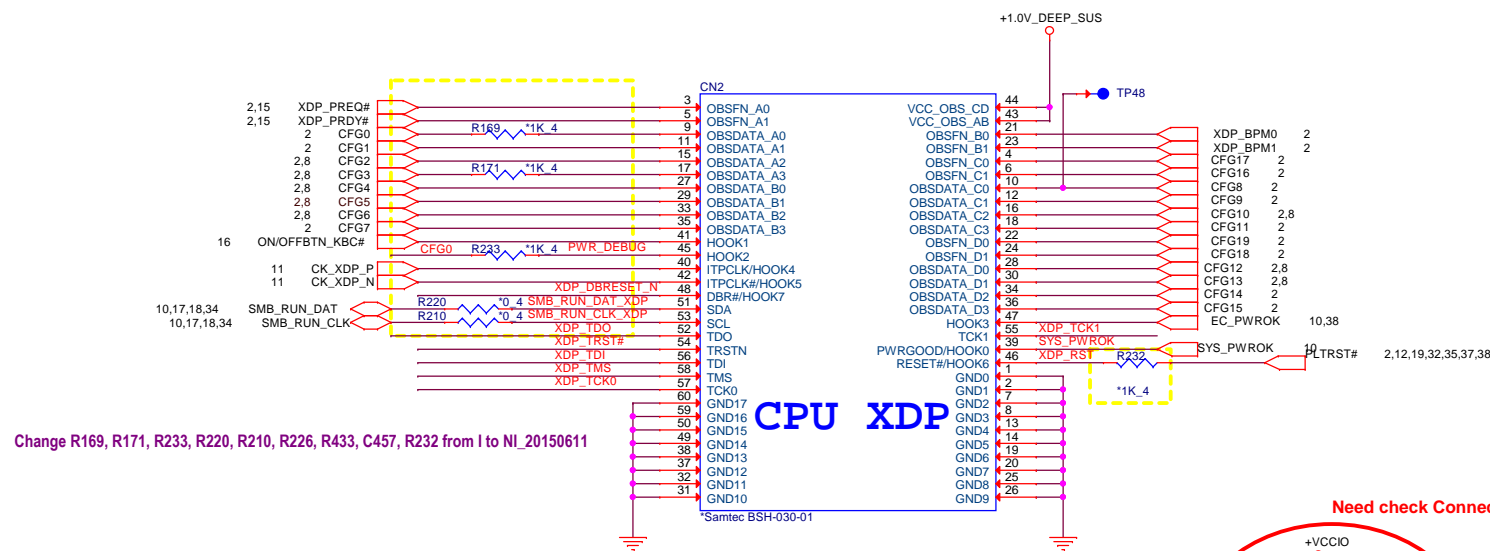




9,10,12,13,16,18 +3V_DEEP_SUS 

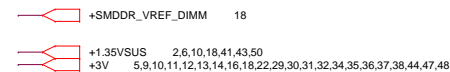
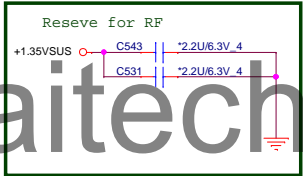


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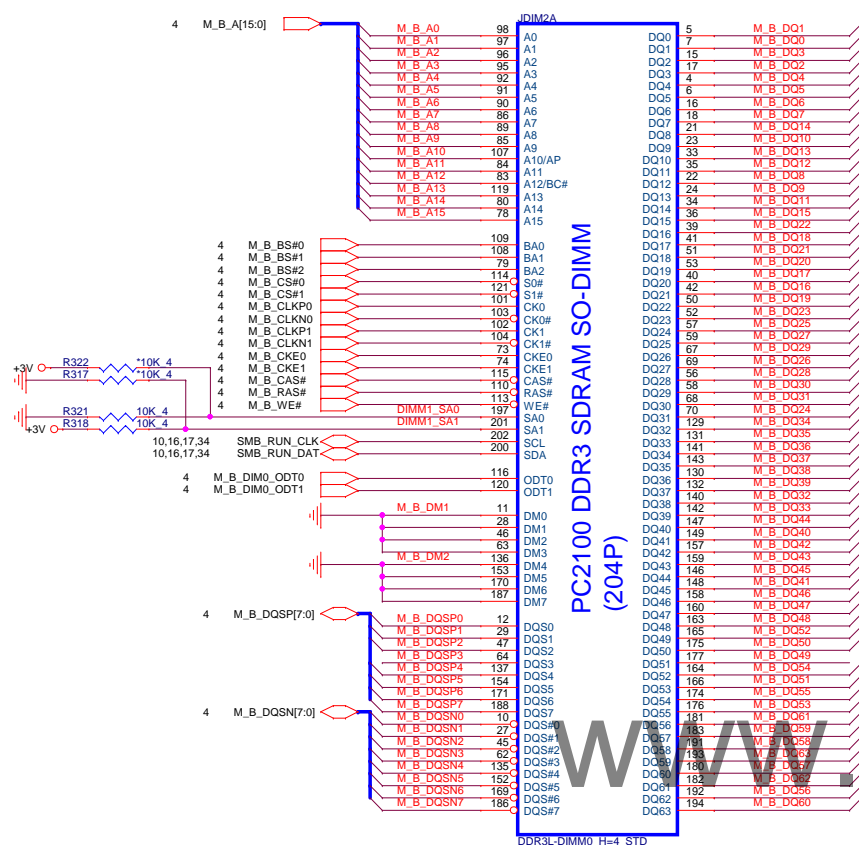


PROJECT : Bellagio
Quanta Computer Inc.

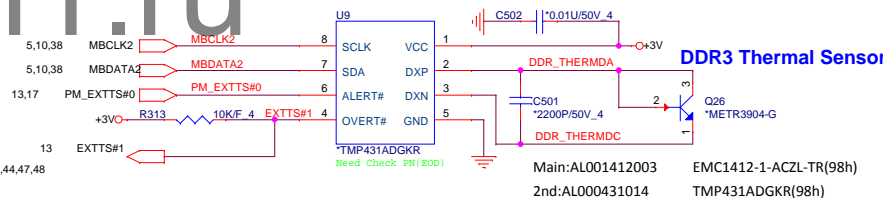
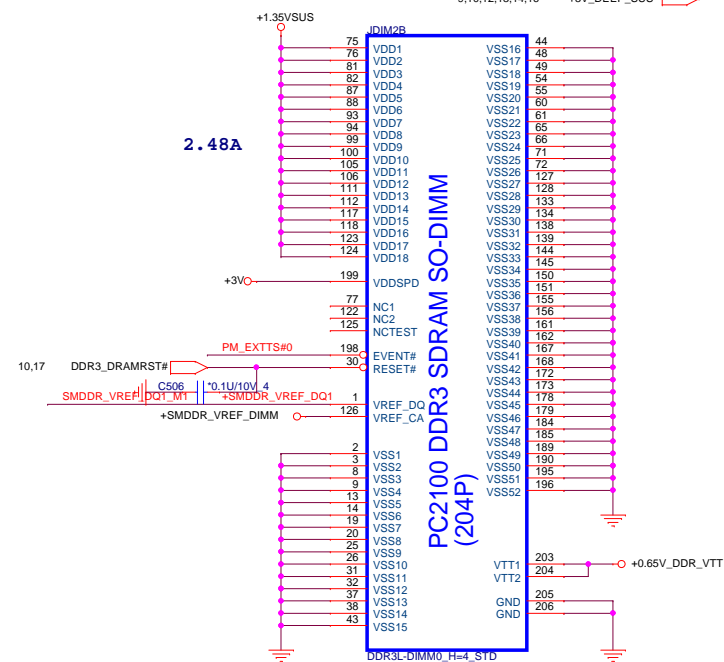
Size	Document Number	Rev
NB5	22 -- HSW XDP & APS	1A
Date: Tuesday, June 16, 2015	Sheet 16 of 51	



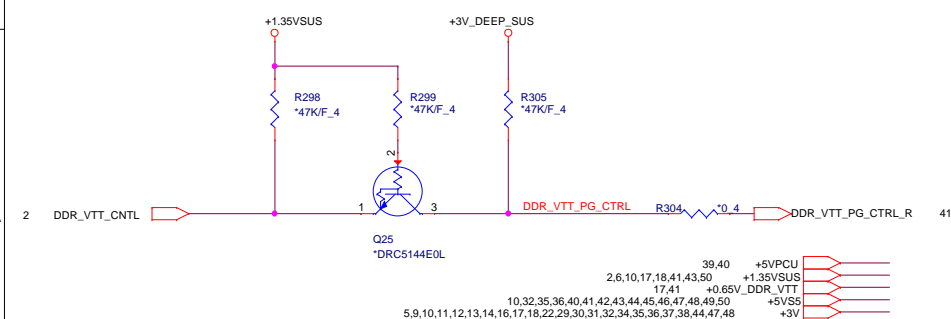
Size Custom	Document Number 17 -- DDR3 DIMM0-STD(4.0H)	Rev 1A
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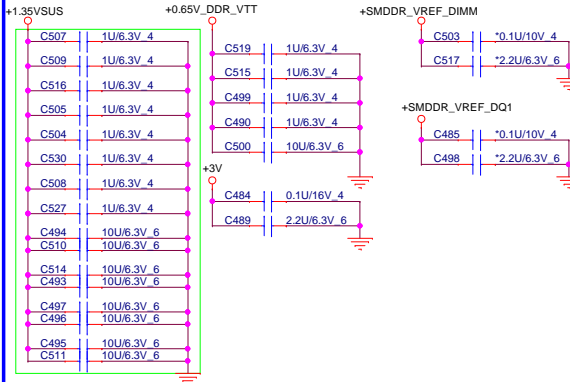


Co-lay for ODT From Intel MOW, ODT directly connection to CPU

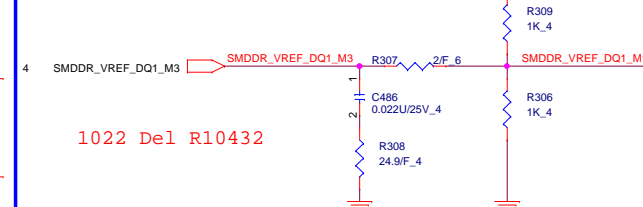


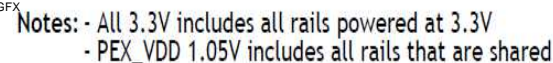
Place these Caps near So-Dimm1.

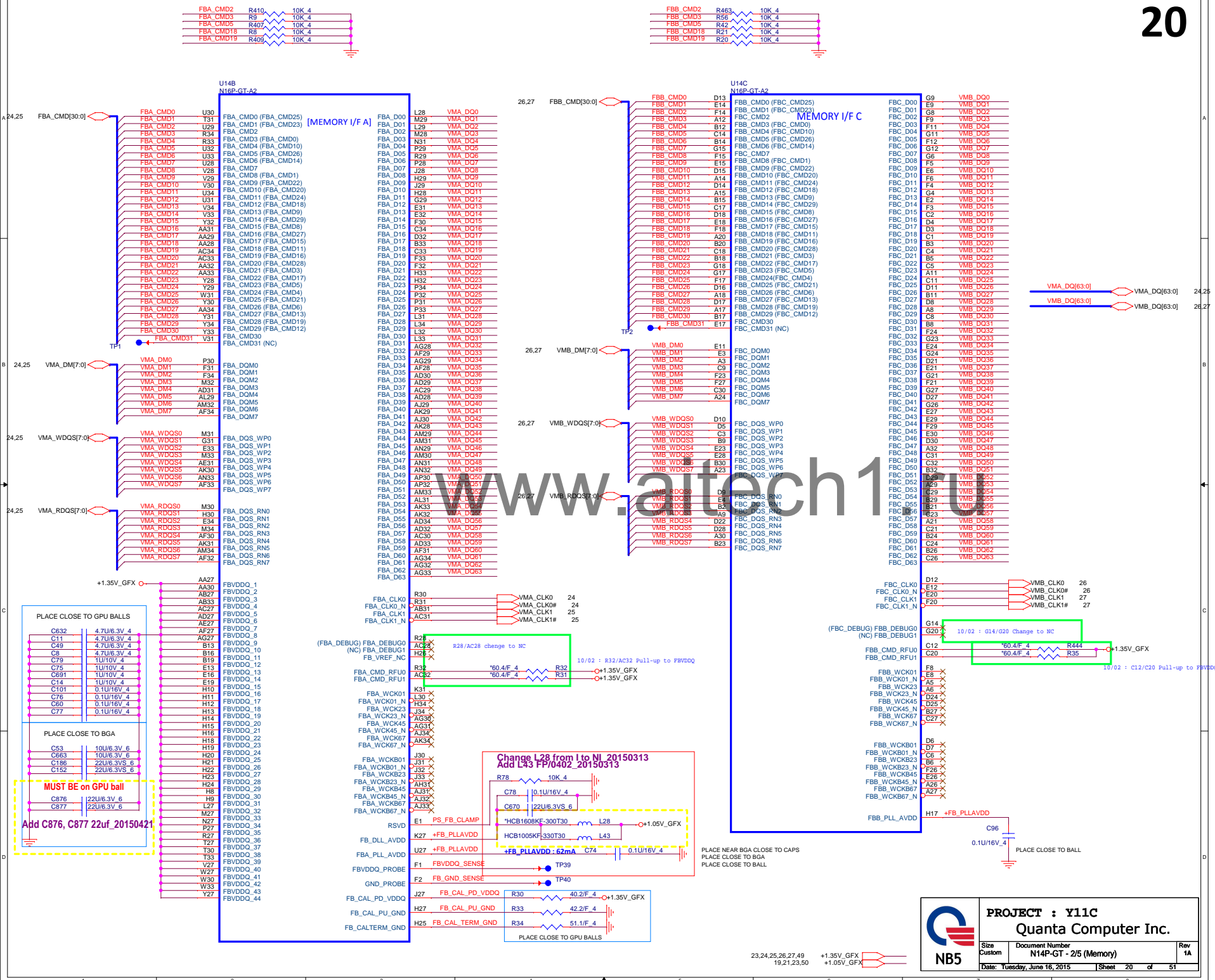
1uF/10uF 4pcs on each side of connector

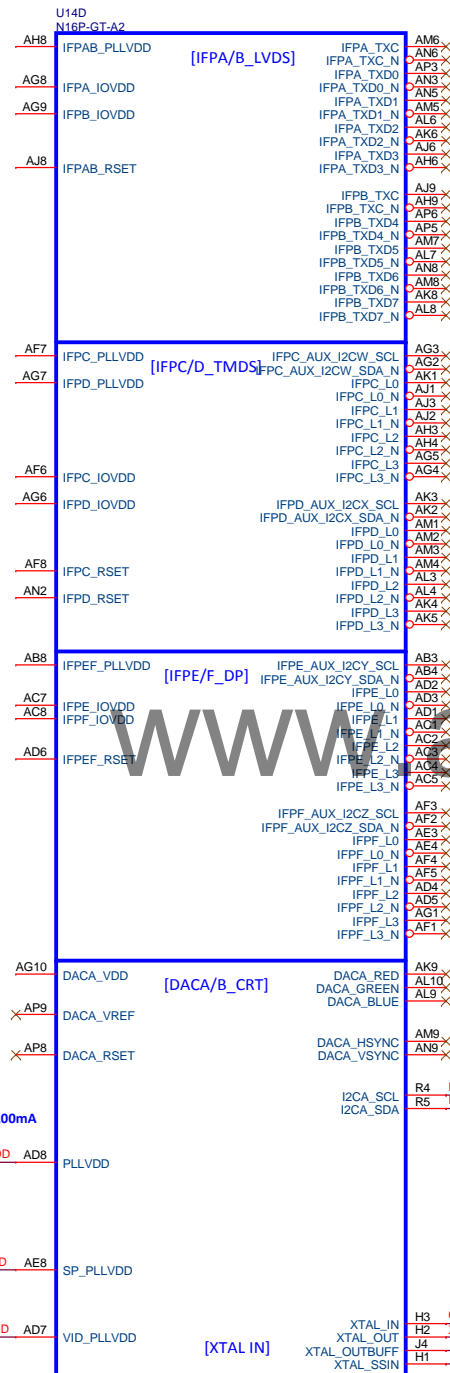


VREF DQ1 M1 Solution



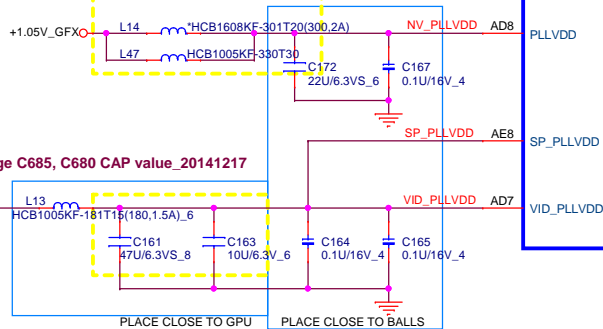




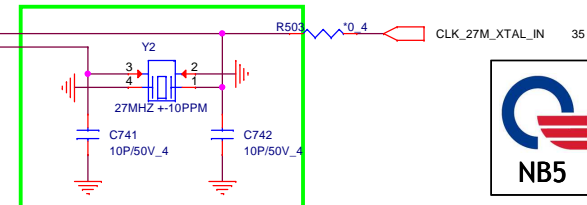


Change L14 from I to NI_20150313
Add L47 FP:0402_20150313

PLLVDD : 200mA

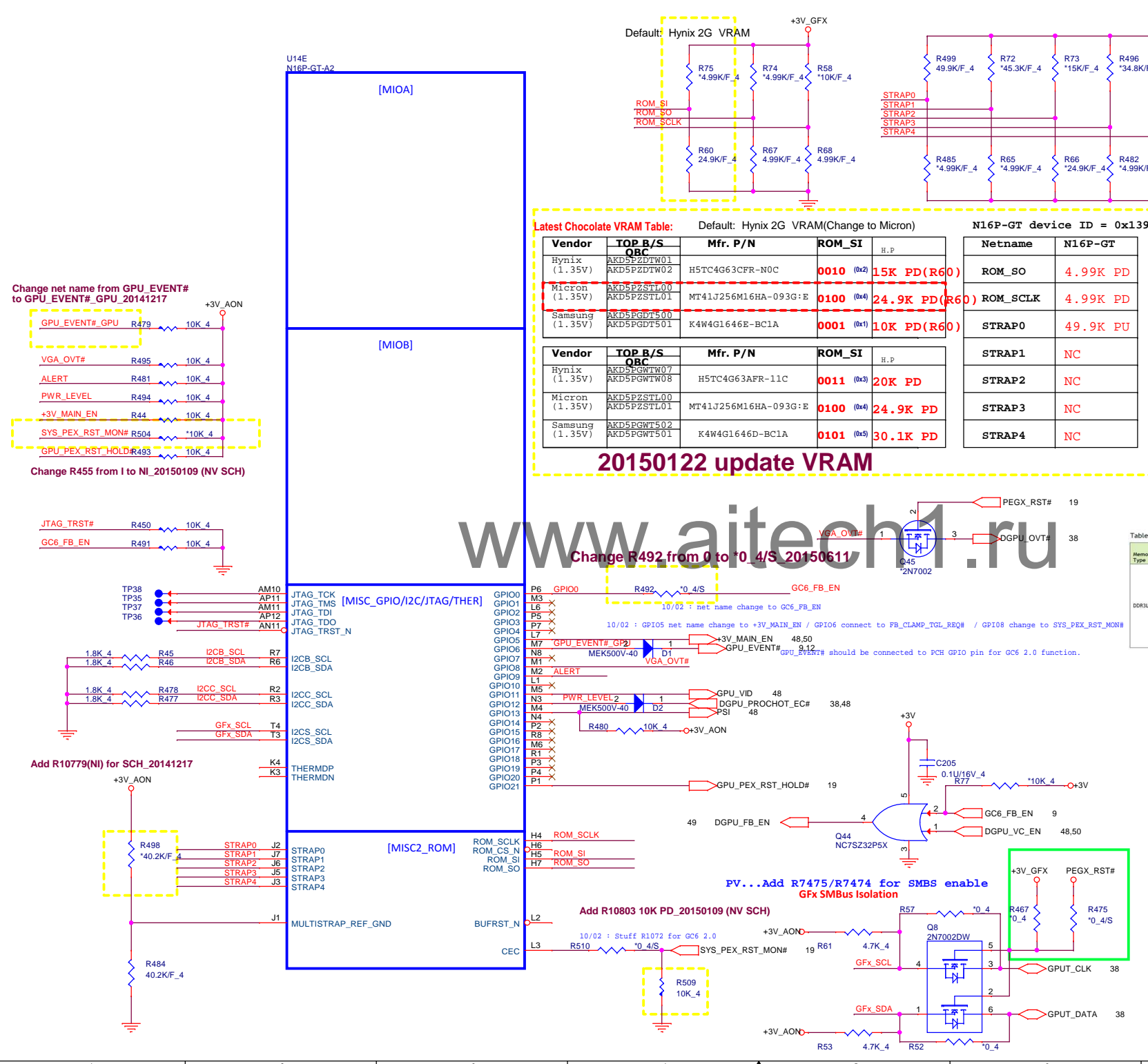


DV2...Change Y1 P/N and footprint
vendor suggest change C117,C84 to 10pf 1/11



PROJECT : Y11C
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Size A3	Document Number N14P-GT - 3/5 (Display)	Rev 1A
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4.99K/F_4: CS24992FB26 RES CHIP 4.99K 1/16W +1% (0402)
 10K/F_4: CS31002FB26 RES CHIP 10K 1/16W +1% (0402)
 15K/F_4: CS31502FB24 RES CHIP 15K 1/16W +1% (0402)
 20K/F_4: CS32002FB29 RES CHIP 20K 1/16W +1% (0402)
 24.9K/F_4: CS32492FB16 RES CHIP 24.9K 1/16W +1% (0402)
 30.1K/F_4: CS33012FB18 RES CHIP 30.1K 1/16W +1% (0402)
 34.8K/F_4: CS33482FB22 RES CHIP 34.8K 1/16W +1% (0402)
 45.3K/F_4: CS34532FB18 RES CHIP 45.3K 1/16W +1% (0402)

Logical Strap Bit Mapping

Table 15-2. Resistance Mapping to Hex Values

Resistor Values	Pull-Up to 3V3_MAIN	Pull-Down to GND
4.99 kΩ	1000	0000
10.0 kΩ	1001	0001
15.0 kΩ	1010	0010
20.0 kΩ	1011	0011
24.9 kΩ	1100	0100
30.1 kΩ	1101	0101
34.8 kΩ	1110	0110
45.3 kΩ	1111	0111

Table 15-4 GB3-256 Multi-Level Mode Strapping

Strap Pin Name	Logical Strapping Bit 3	Logical Strapping Bit 2	Logical Strapping Bit 1	Logical Strapping Bit 0
ROM_SCLK	PCI_DEVID[4]	SUB_VENDOR	PCI_DEVID[5]	PEX_PLL_EN_TERM
ROM_SI	RAM_CFG[3]	RAM_CFG[2]	RAM_CFG[1]	RAM_CFG[0]
ROM_SO	FB[1]	FB[0]	SMB_ALT_ADDR	VGA_DEVICE
STRAP0	USER[3]	USER[2]	USER[1]	USER[0]
STRAP1	3GIO_PADCFG[3]	3GIO_PADCFG[2]	3GIO_PADCFG[1]	3GIO_PADCFG[0]
STRAP2	PCI_DEVID[3]	PCI_DEVID[2]	PCI_DEVID[1]	PCI_DEVID[0]
STRAP3	SOR3_EXPOSED	SOR2_EXPOSED	SOR1_EXPOSED	SOR0_EXPOSED
STRAP4	RESERVED	PCIE_SPEED_CHANGE_GE13	PCIE_MAX_SPEED	DP_PLL_VDD3V

Recommended Memories

Configuration	Vendor	Manufacturer Part Number	Die Revision	Strap	Memory Speed (MHz)	Memory Data Code Minimum	Status
Single Rank	Hynix	H5TC2633FR-11C	F-die	0x9	900	N/A	Production candidate
	Micron	MT41J128M16JT-093G:K	K-die	0xA	900	1322	Production candidate
	Samsung	K4W2G1640Q-BC1A	Q-die	0xB	900	N/A	Production candidate
Single Rank	Hynix	H5TC4633FR-11C	A-die	0x9	900	N/A	Production candidate
	Micron	MT41J256M16HA-093G:E	E-die	0xA	900	1322	Production candidate
	Samsung	K4W4G16440-BC1A	D-die	0x5	900	N/A	Production candidate

Hynix

Samsung

C die 0x2

E die 0x1

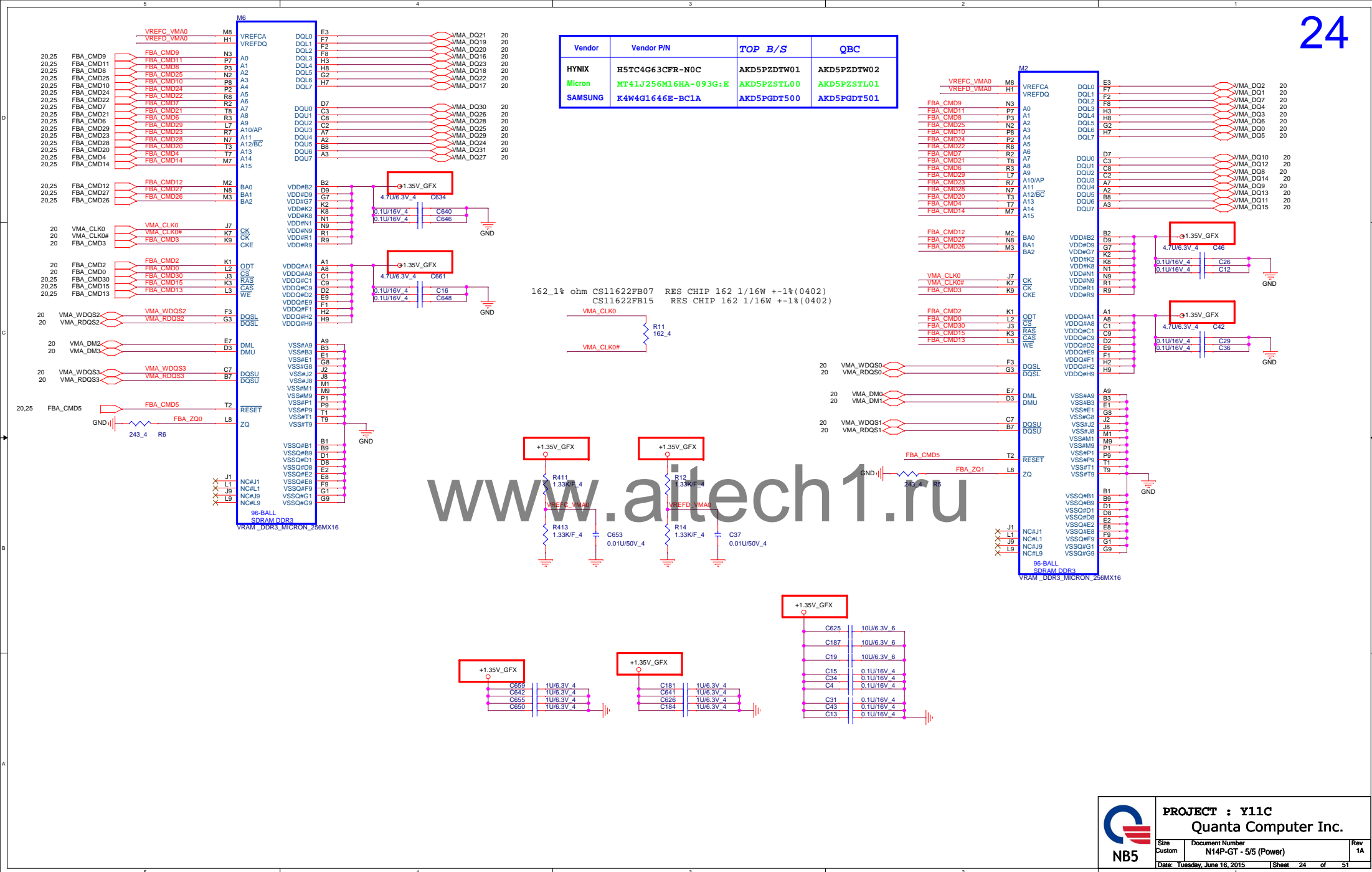
Hynix Samsung C die 0x2 E die 0x1

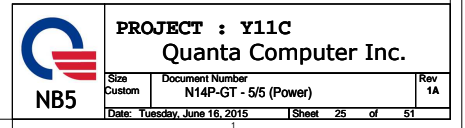
GPIO ASSIGNMENTS

GPIO	SM107/GMT08	GK208/GK107
GPIO0	GCS_FB_EN	GPIO_FB_CLAMP
GPIO1	MEM_VDD_CTL	MEM_VDD_CTL
GPIO2	LCD_BL_PWM	LCD_BL_PWM
GPIO3	LCD_PWR_EN	LCD_PWR_EN
GPIO4	LCD_BL_EN	LCD_BL_EN
GPIO5	GCS_PWR_EN	DEBUG SERVICE HEADER
GPIO6	GPU_EVENT#	Remote Sensor Error Correction
GPIO7	DEBUG SERVICE HEADER	3D STEREO
GPIO8	SYS_PEX_RST_MON#	GPU OVERTEMP
GPIO9	Remote Sensor Error Correction	GPU THERMAL_ALERT/FAN_PWM
GPIO10	MEM_VREF_CTL(N/A)	MEM_VREF_CTL
GPIO11	NVDDO_PWM_VDD	NVDDO_PWM_VDD
GPIO12	AC DETECT	AC DETECT
GPIO13	NVDDO_PSI	NVDDO_PSI
GPIO14	IFPG_HDP	N/C
GPIO15	N/C	N/C
GPIO16	FRAME LOCK	FRAME LOCK
GPIO17	IFPD_HDP(OP1M)	IFPD_HDP(OP1M)
GPIO18	IFPE_HDP(OP1M)	IFPE_HDP(OP1M)
GPIO19	IFPF_HDP(OP1M)	IFPF_HDP(OP1M)
GPIO20	GCS_MODE	N/A
GPIO21	GPU_PEX_RST_HOLD#	N/A

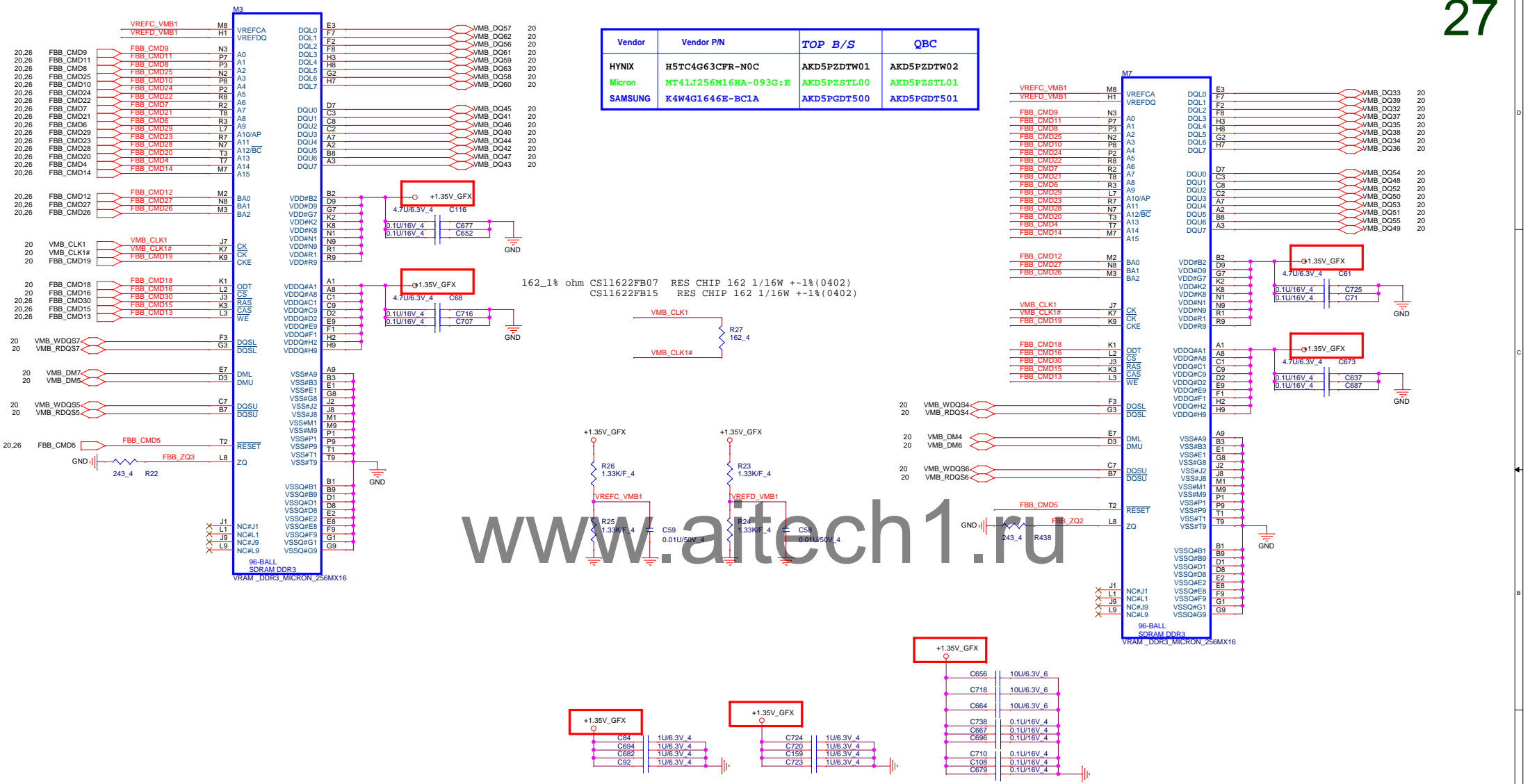
48 +VGACORE
19,20,21,50 +1.05V_GFX
20,24,25,26,27,49 +1.35V_GFX
19,22,48,50 +3V_GFX
5,36,37,38,44,47,48 +3V

Vendor	Vendor P/N	TOP B/S	QBC
HYNIX	H5TC4G63CFR-N0C	AKD5P2DWT01	AKD5P2DWT02
Micron	MT41LJ256ML16HA-093G:E	AKD5P2STL00	AKD5P2STL01
SAMSUNG	K4W4G1646E-BC1A	AKD5PGDT500	AKD5PGDT501





Vendor	Vendor P/N	TOP B/S	QBC
HYNIX	H5TC4G63CFR-N0C	AKD5PZDTW01	AKD5PZDTW02
Micron	MT41J256M16HA-093G:E	AKD5PZSTL00	AKD5PZSTL01
SAMSUNG	K4W4G1646E-BC1A	AKD5PGDT500	AKD5PGDT501



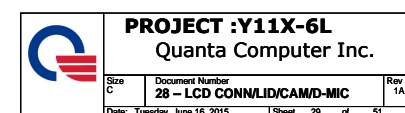
Del DP to LVDS IC_20150310

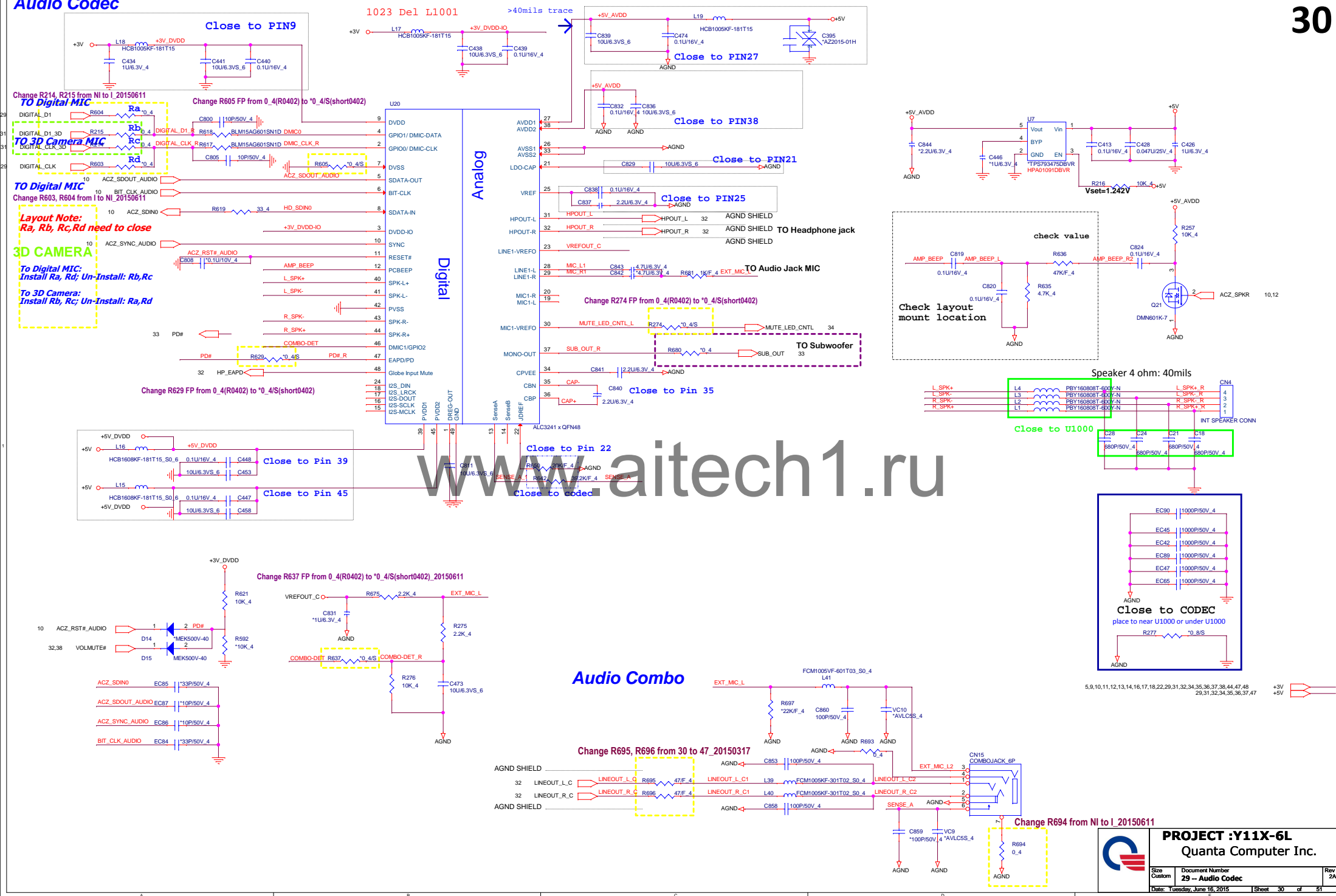
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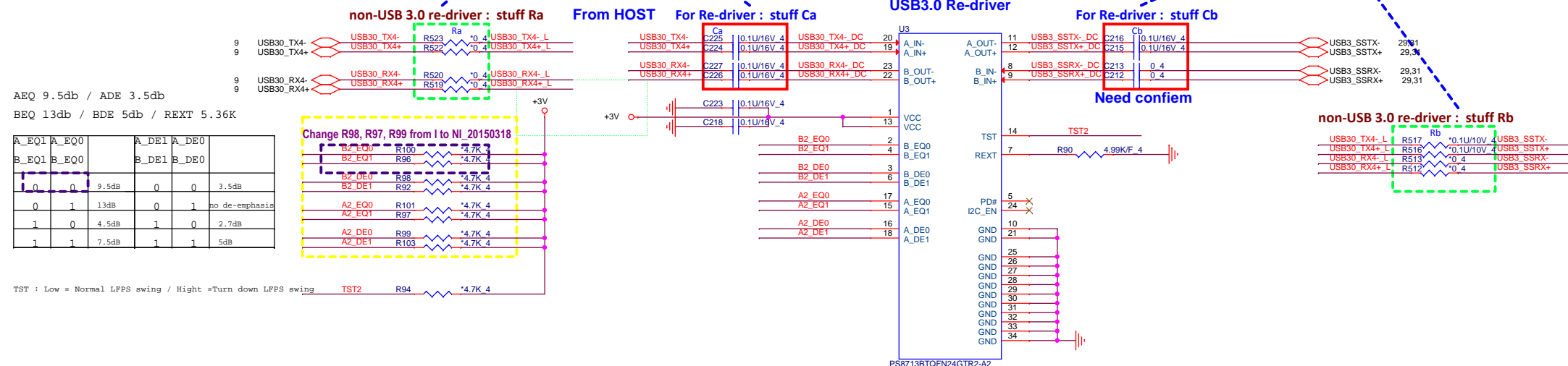
Size C	Document Number 28 - LCD CONN/LID/CAM/D-MIC	Rev 1A
Date: Tuesday, June 16, 2015	Sheet 28 of 51	

LVDS Conn.



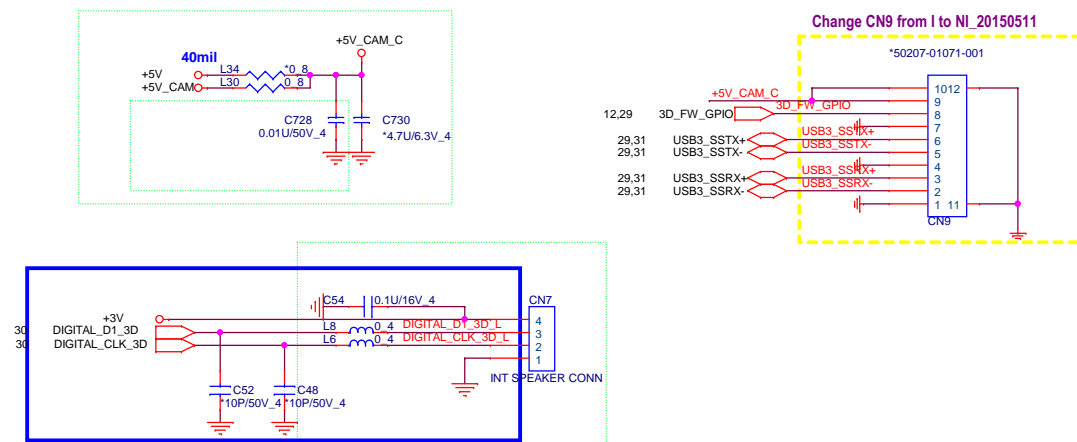
USB 3.0 re-driver for 3D Camera

3D Camera CON & USB3 re-driver for 3D.



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3D Camera Conn.



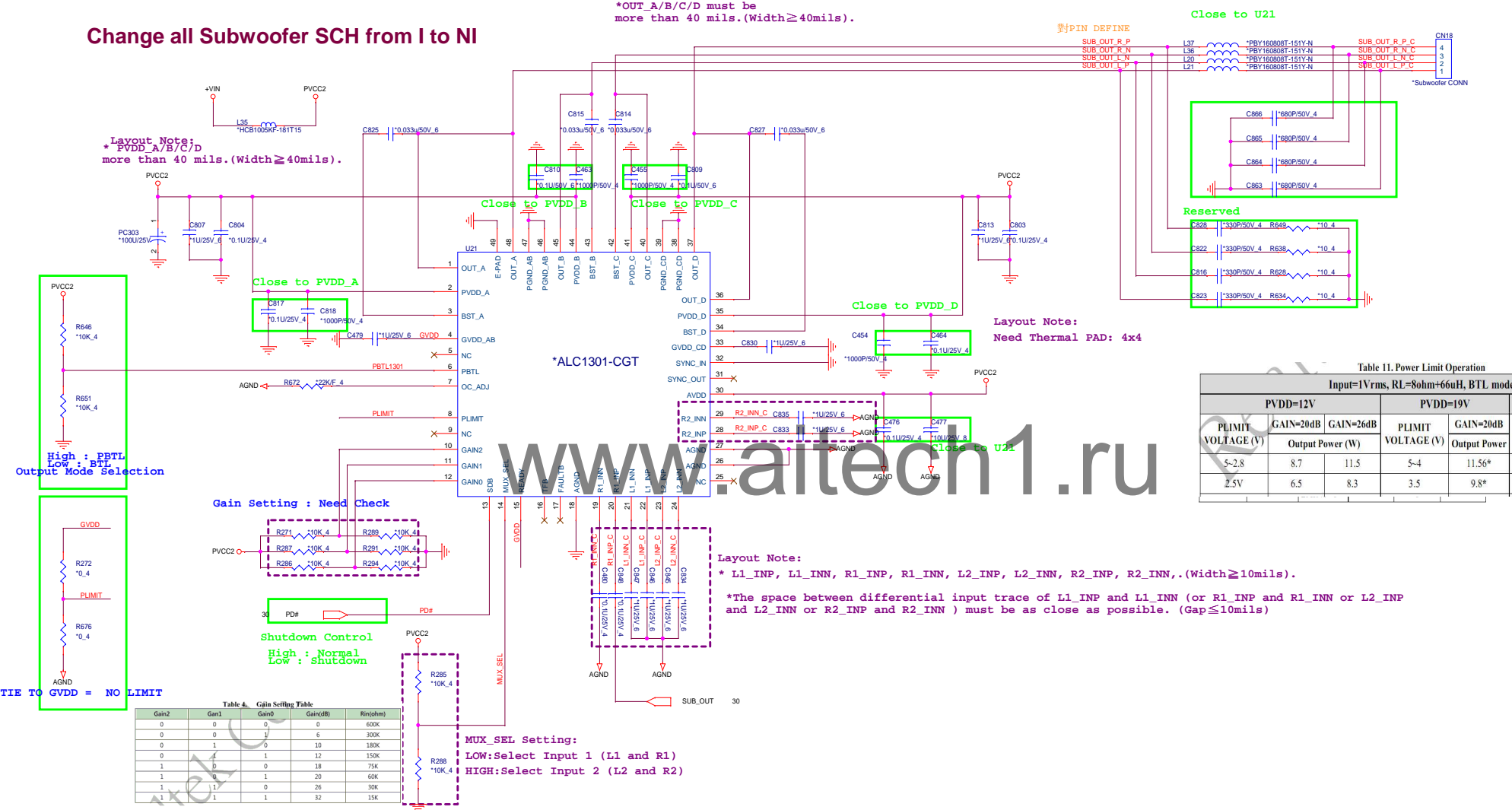
For Power Crunch 15" Subwoofer

Change all Subwoofer SCH from I to NI

Layout Note:

*The space between differential output trace of OUT_A and OUT_B (or OUT_C and OUT_D) must be as close as possible. ($\leq 10\text{mils}$)

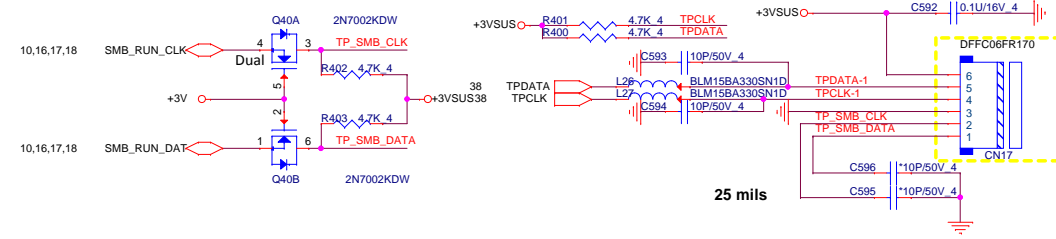
*OUT_A/B/C/D must be more than 40 mils. ($\text{Width} \geq 40\text{mils}$). Close to U21



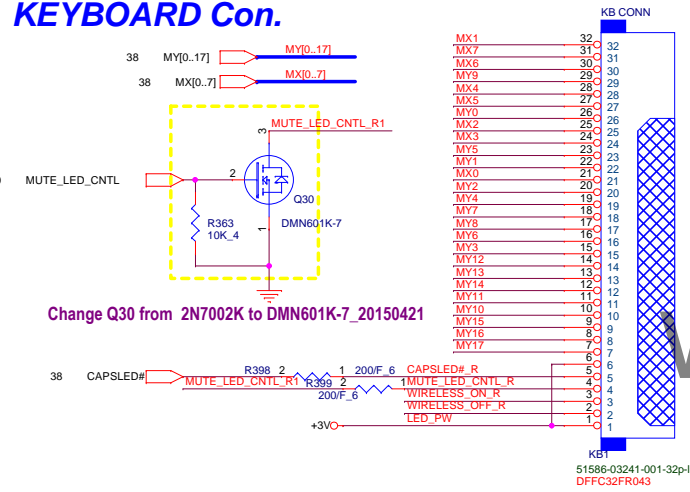
Input=1Vrms, RL=8ohm+66uH, BTL mode					
PVDD=12V			PVDD=9V		
PLIMIT VOLTAGE (V)	GAIN=20dB	GAIN=26dB	PLIMIT VOLTAGE (V)	GAIN=20dB	GAIN=26dB
	Output Power (W)		Output Power (W)	Output Power	
5-2.8	8.7	11.5	5-4	11.56*	
2.5V	6.5	8.3	3.5	9.8*	

Touch Pad Connector

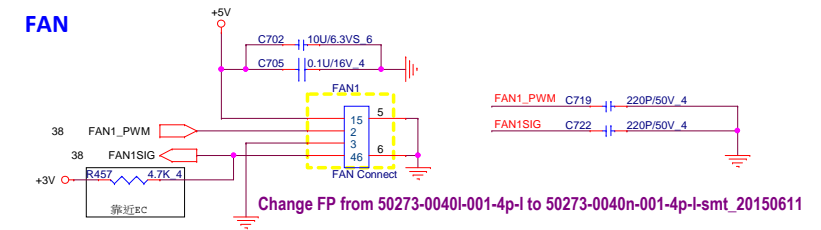
34



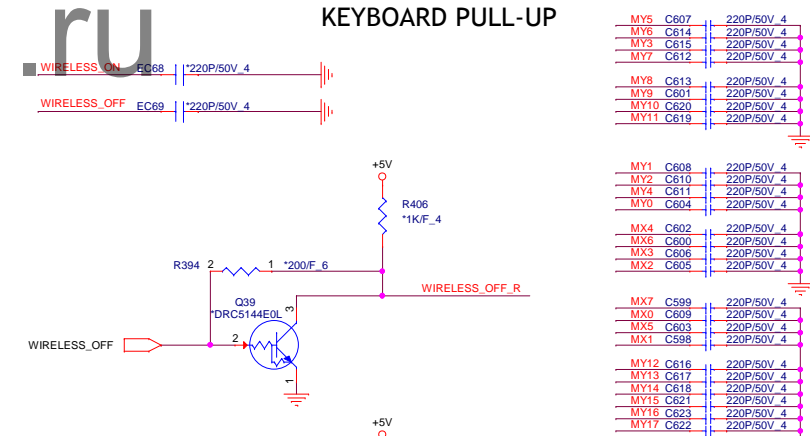
KEYBOARD Con.



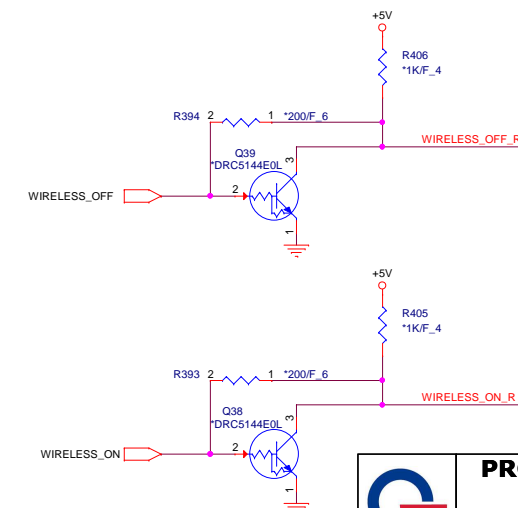
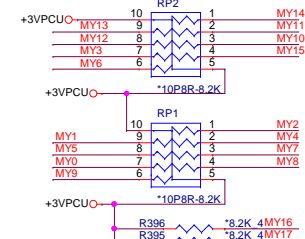
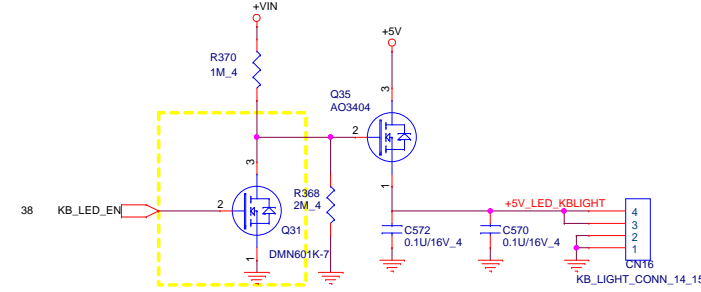
FAN



KEYBOARD PULL-UP



Change Q31 from 2N7002K to DMN601K-7_20150421



PROJECT :Y11X-6L			
Quanta Computer Inc.			
Size	Document Number	Rev	
Custom	31 - PB/TP/KB/FAN/EMI Cap	1A	
Date: Tuesday, June 16, 2015	Sheet	34	of 51

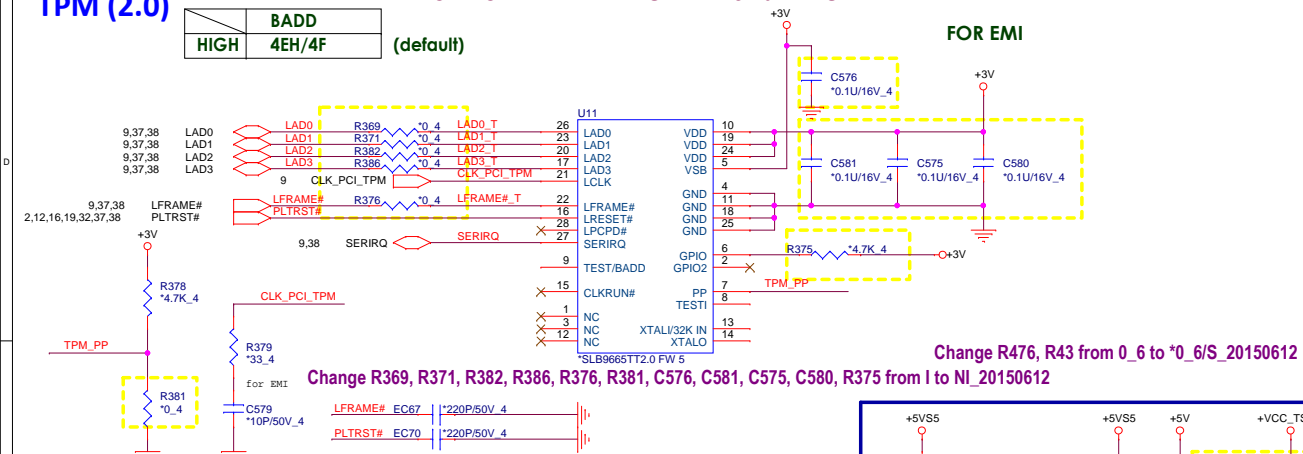
TPM (2.0)

	BADD
HIGH	4EH/4F

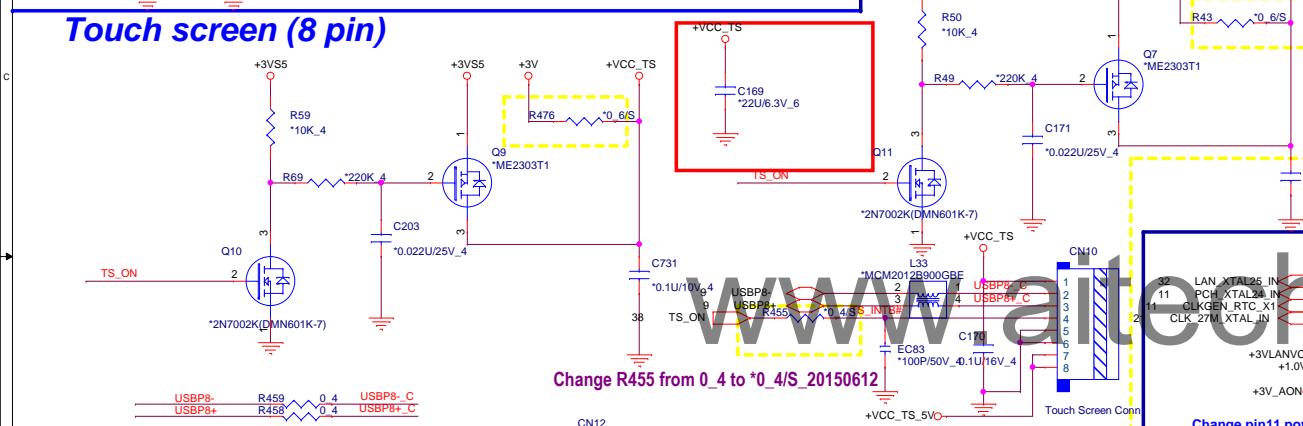
(default)

CHECK DB TPM INSTALL or un-INSTALL

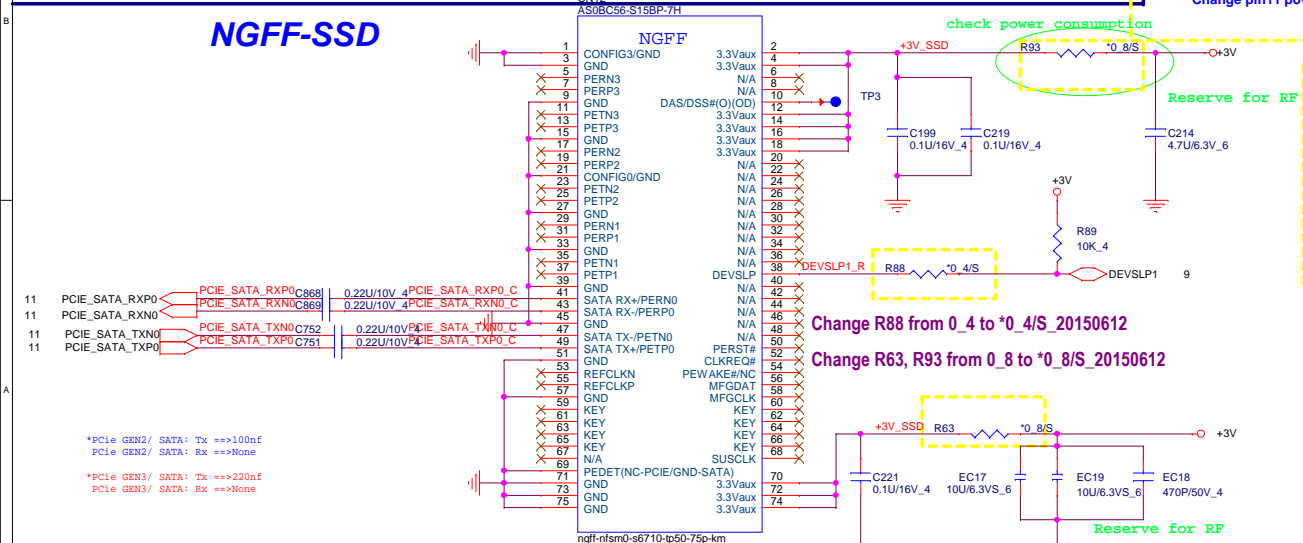
FOR EMI



Touch screen (8 pin)



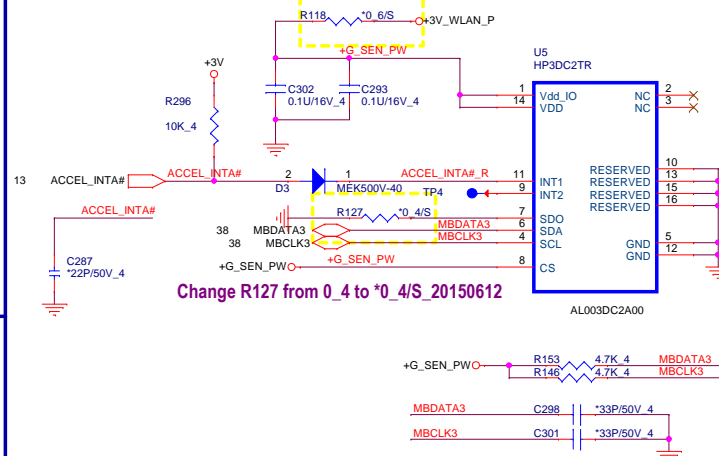
NGFF-SSD



Accelerometer Sensor

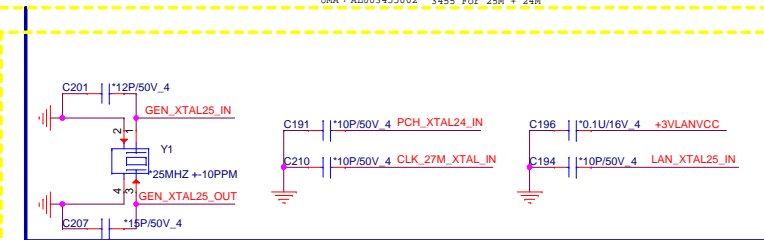
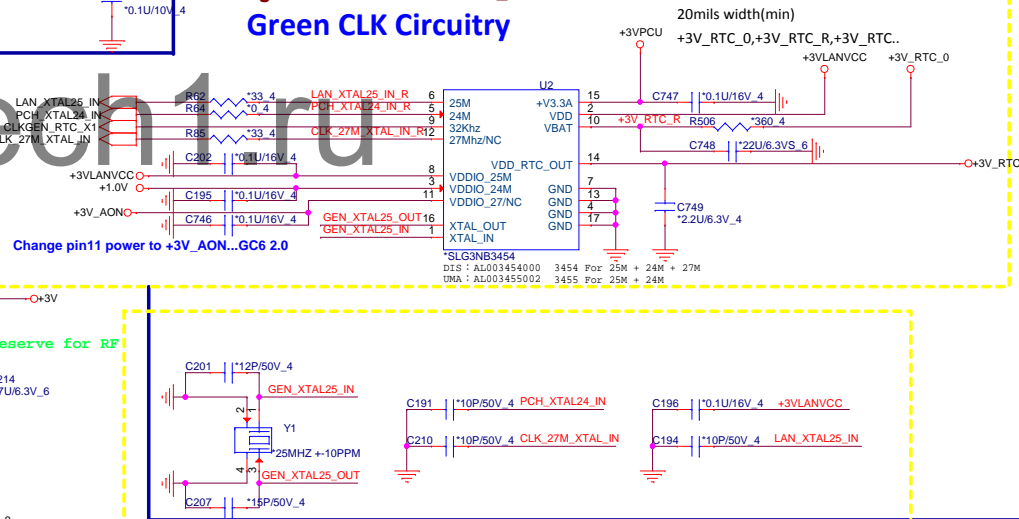
Change R118 from 0_6 to *0_6/S_20150612

G-Sensor Power need check



Change Green CLK from I to NI_20150129

Green CLK Circuitry

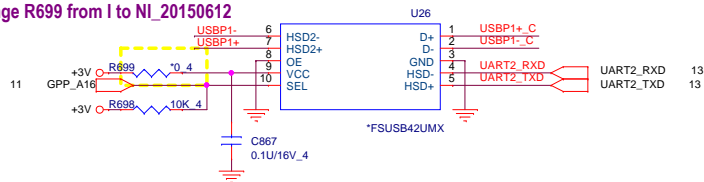


PROJECT :Y11X-6L
Quanta Computer Inc.

Size Custom	Document Number 32 -- TPM/G-Sensor/G-CLK/TS/FP	Rev 1A
Date: Tuesday, June 16, 2015	Sheet 35 of 51	

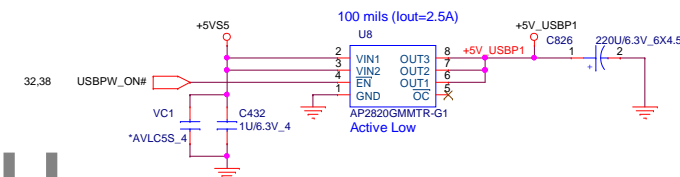
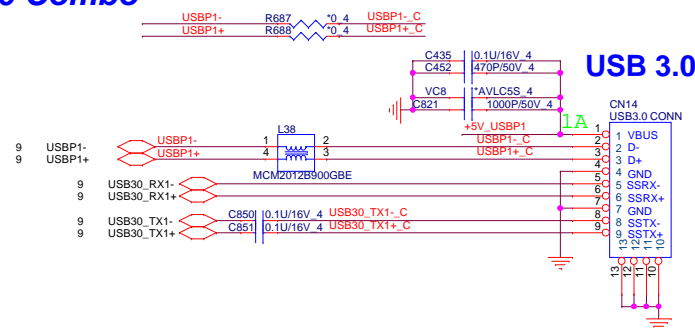
UART for DEBUG

Change R699 from I to NI_20150612



Change R455 from 0_4 to *0_4/S_20150612

USB 2.0/3.0 Combo

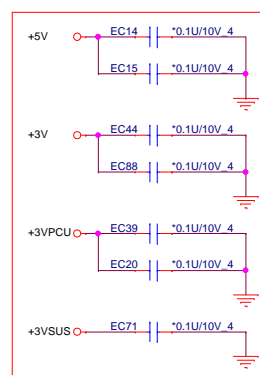


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EMI CAP



Reserve EMI CAP



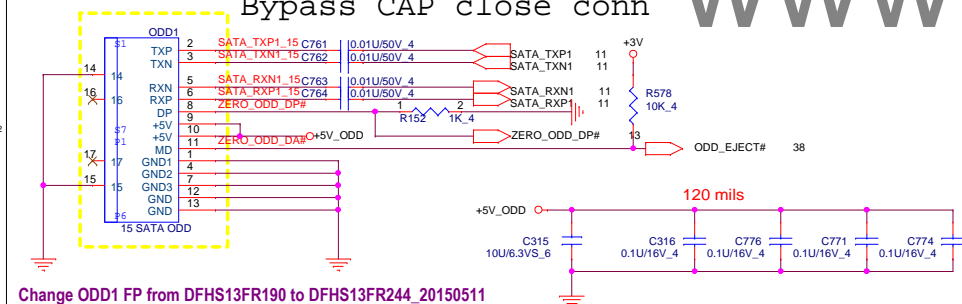
PROJECT :Y11X-6L
Quanta Computer Inc.

Size Custom	Document Number 34 -- USB 3.0	Rev 1A
Date: Tuesday, June 16, 2015	Sheet 36 of	51

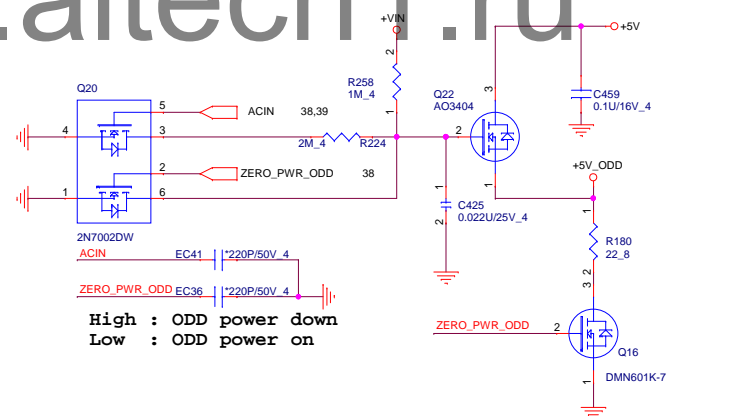
Support Wake Function(Reserve)



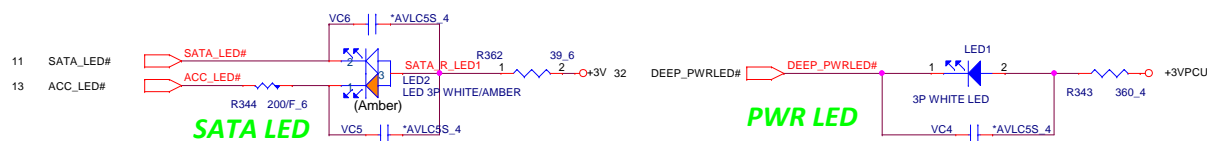
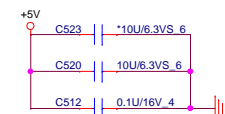
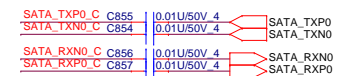
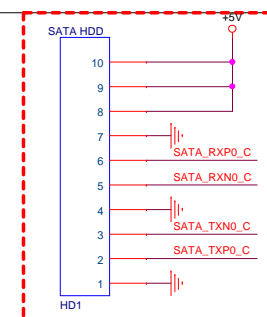
Bypass CAP close conn



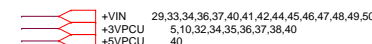
Change ODD1 FP from DFHS13FR190 to DFHS13FR244 20150511

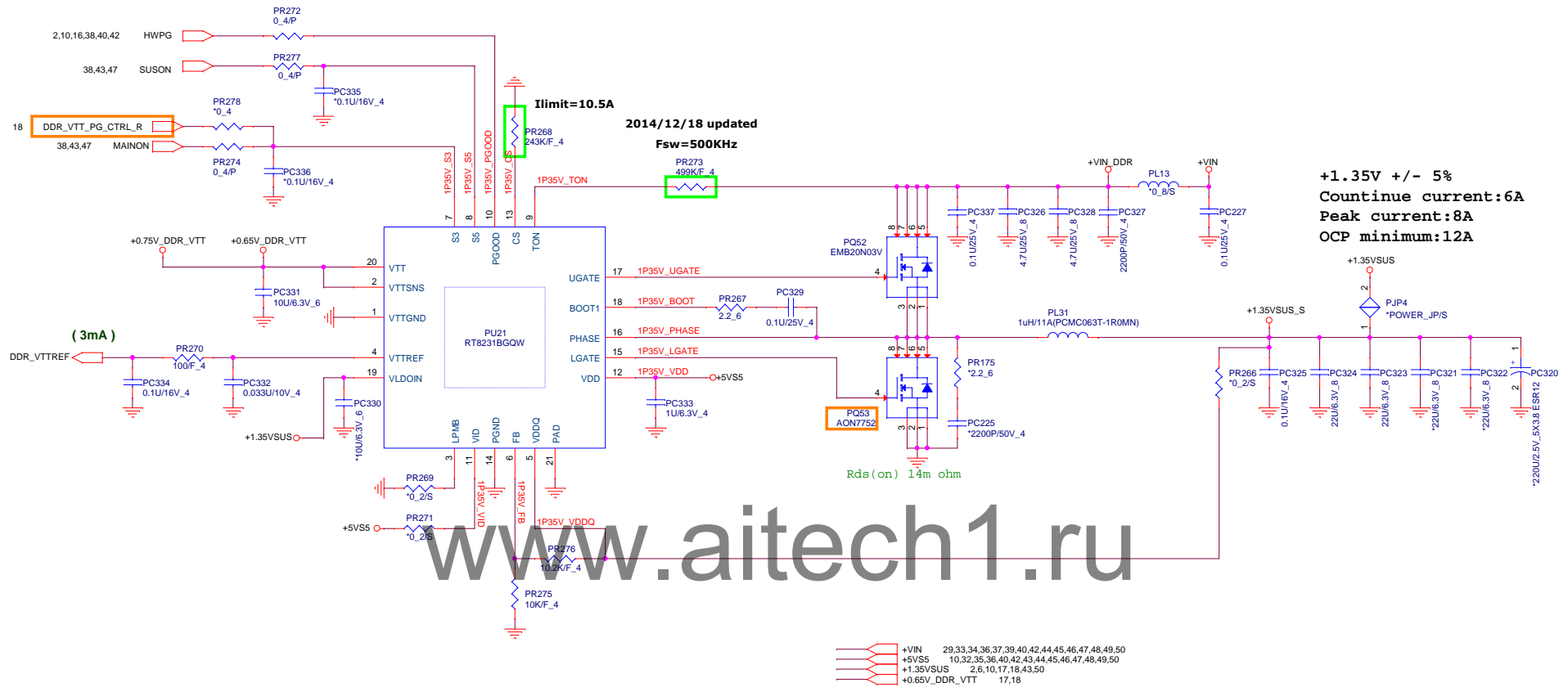


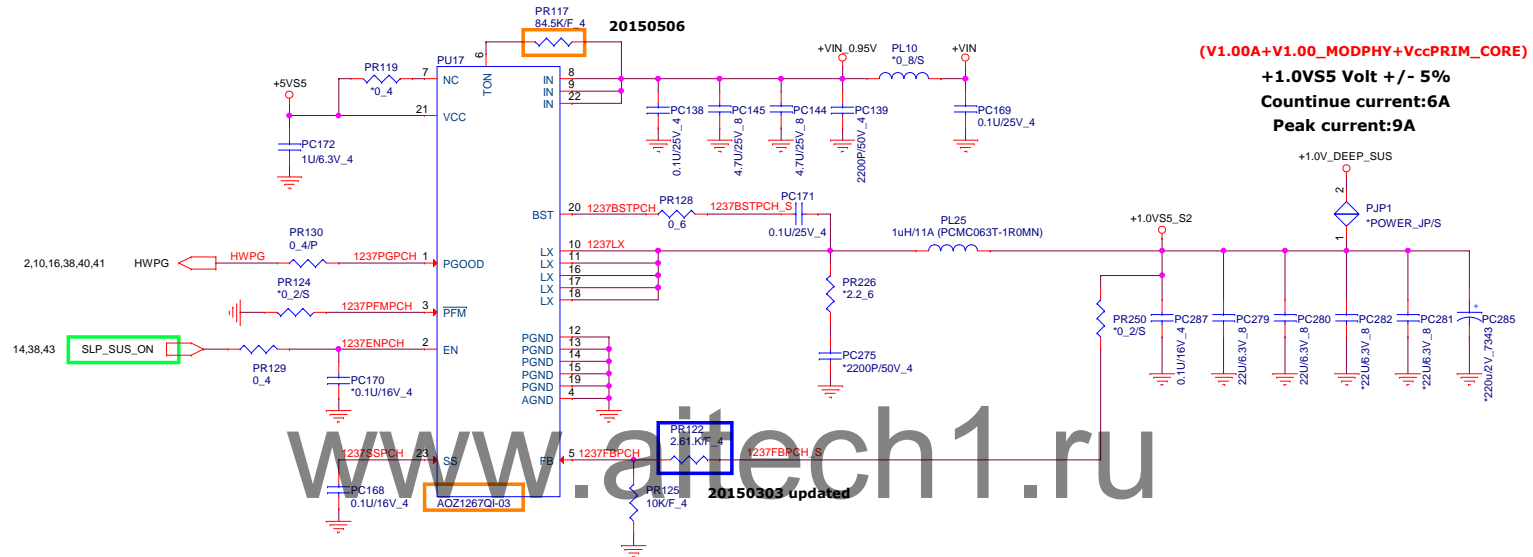
High : ODD power down
Low : ODD power on

HDD

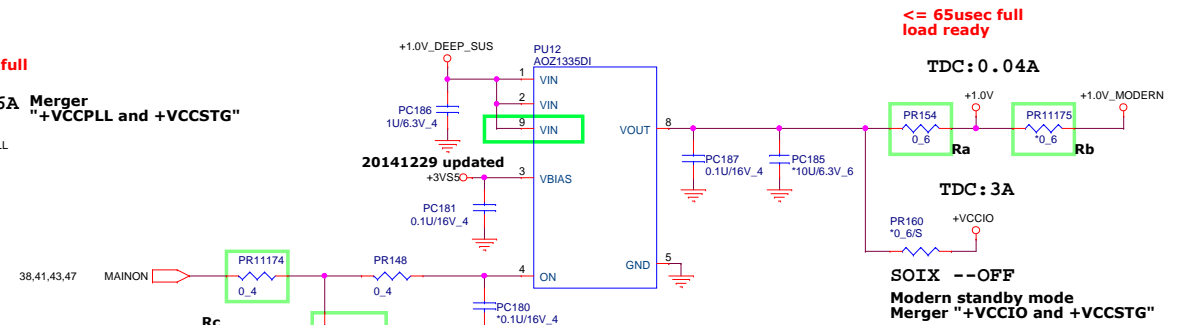
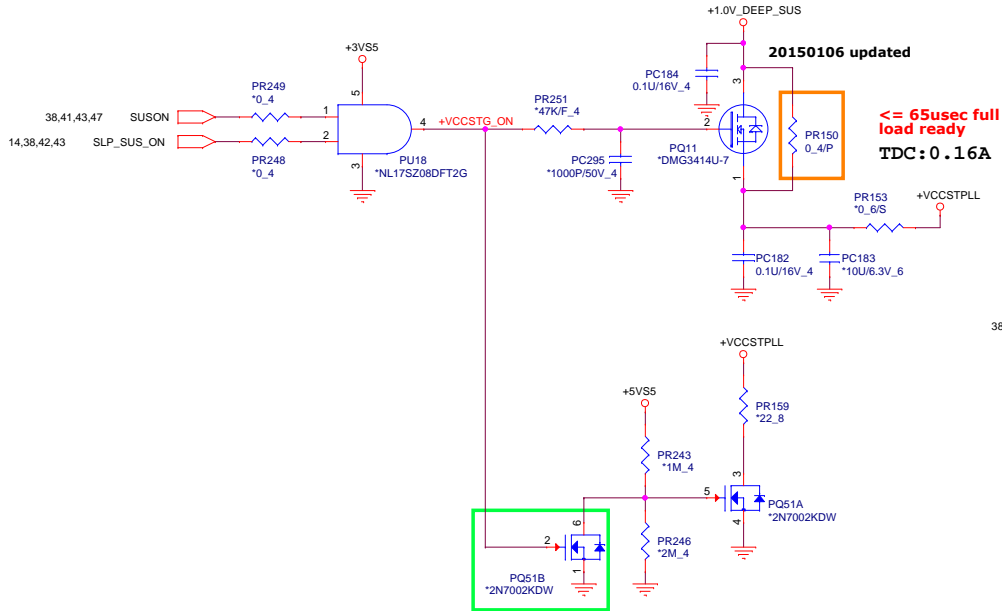








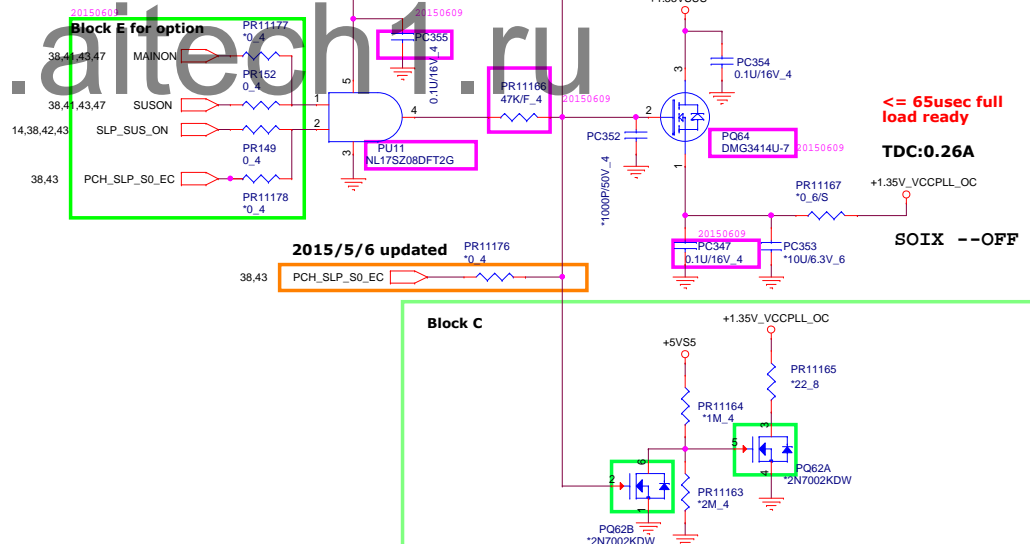
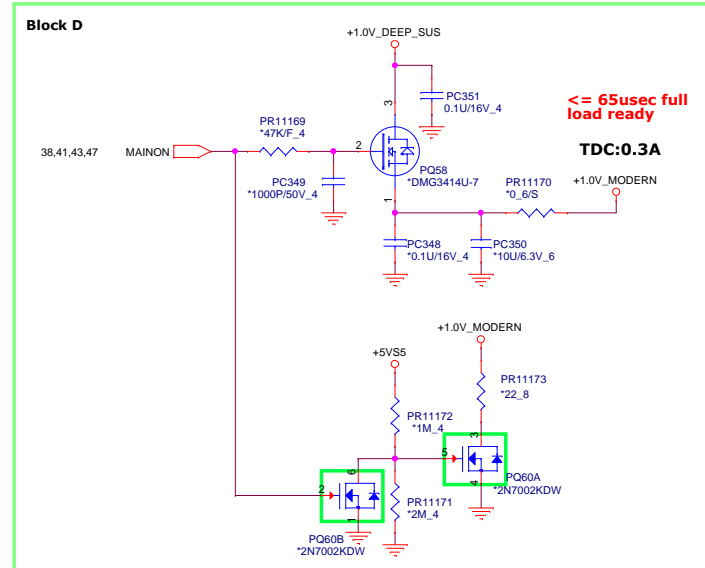
+1.0V 2,5,6,10,16,35,38
 +3VS5 10,12,14,16,35,37,38,40,47,50
 +5VS5 10,32,35,36,40,41,42,44,45,46,47,48,49,50
 +VCCIO 3,6,16
 +VCCSTPLL 2,6,44
 +1.0V_DEEP_SUS 10,11,14,16,42

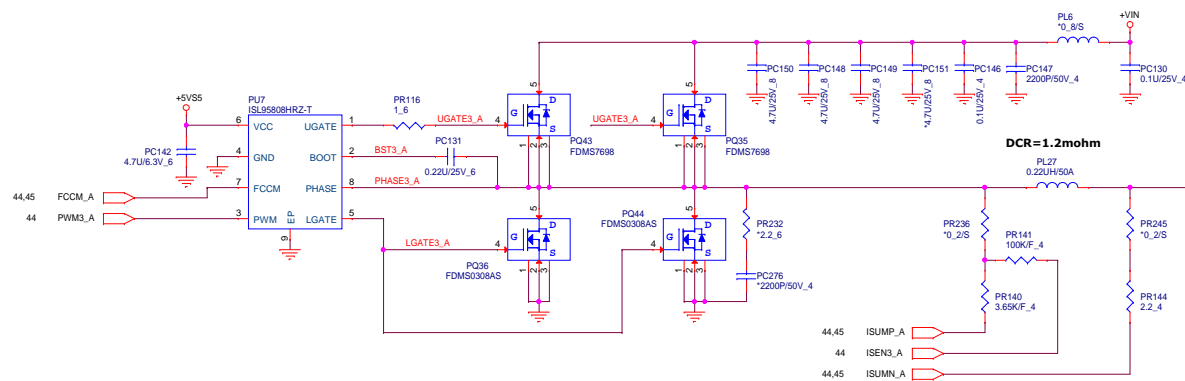
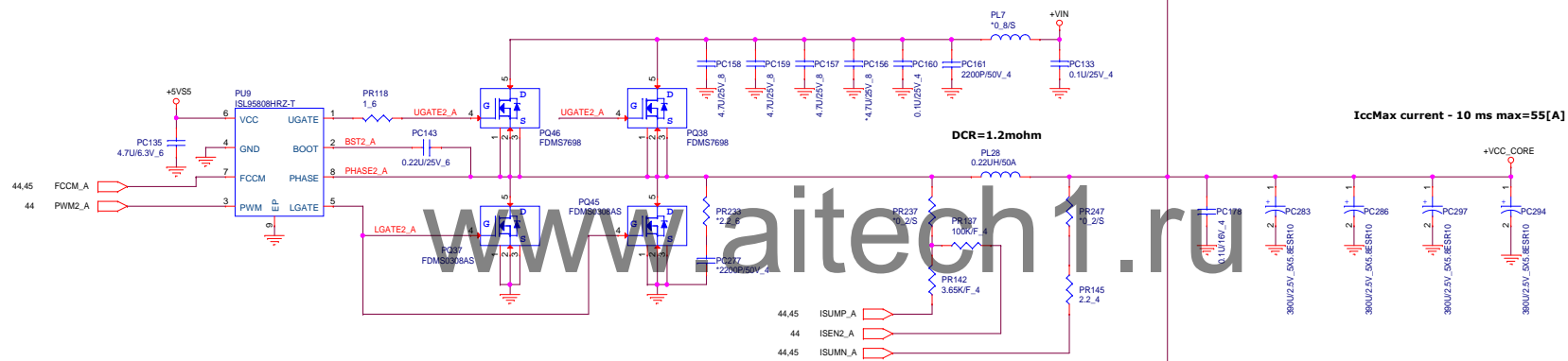
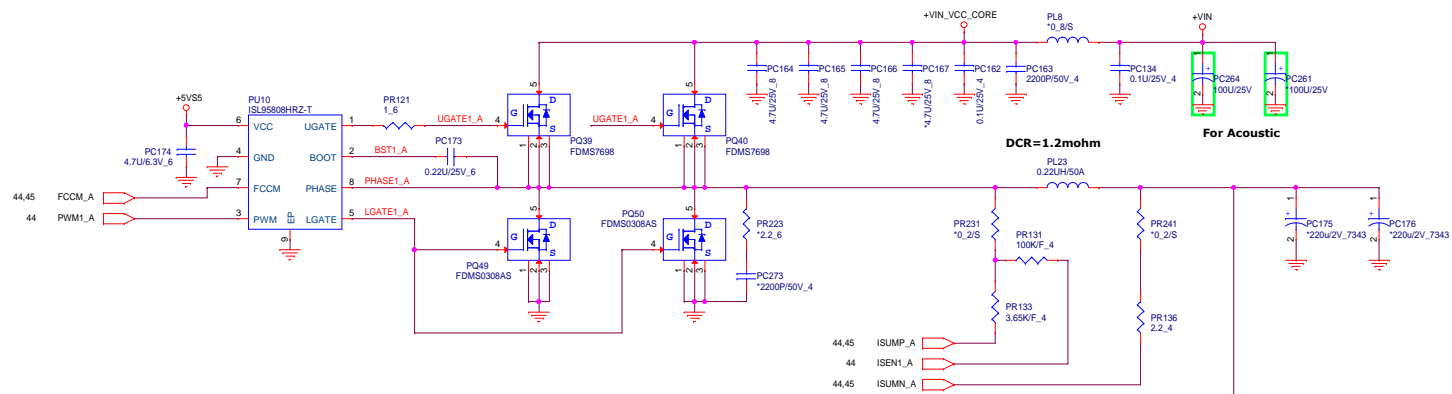


Support Modern standby mode

1. Remove Ra/Rc & stuff Rb/Rd
2. stuff block C & D
3. Block E for option

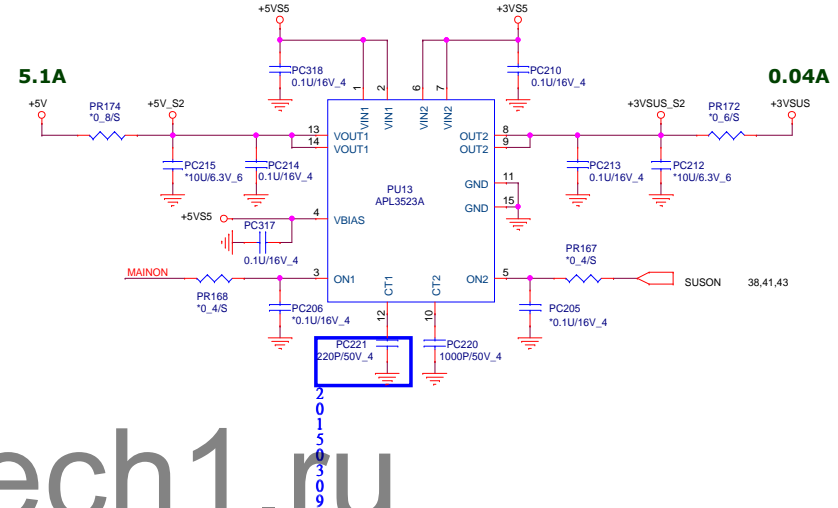
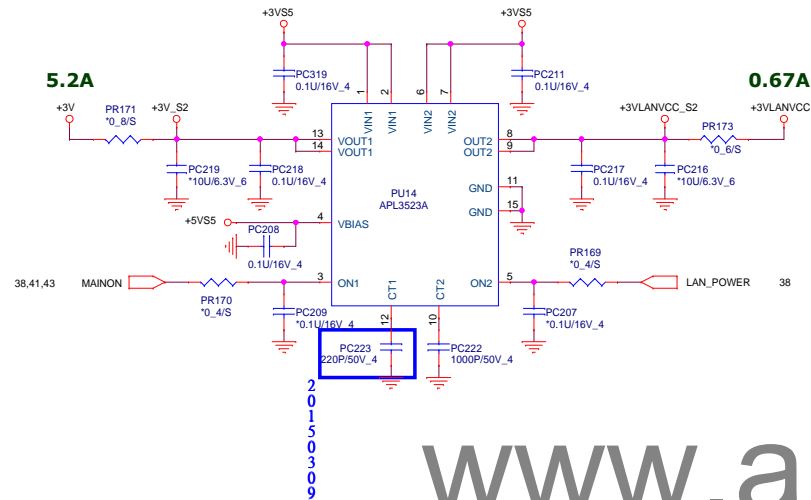
Reserve for Modern StandBy



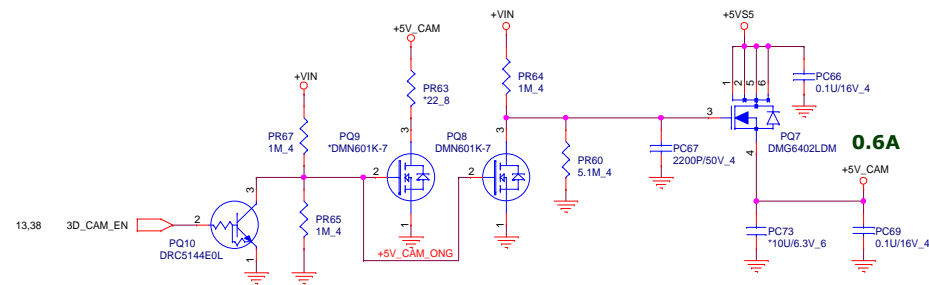


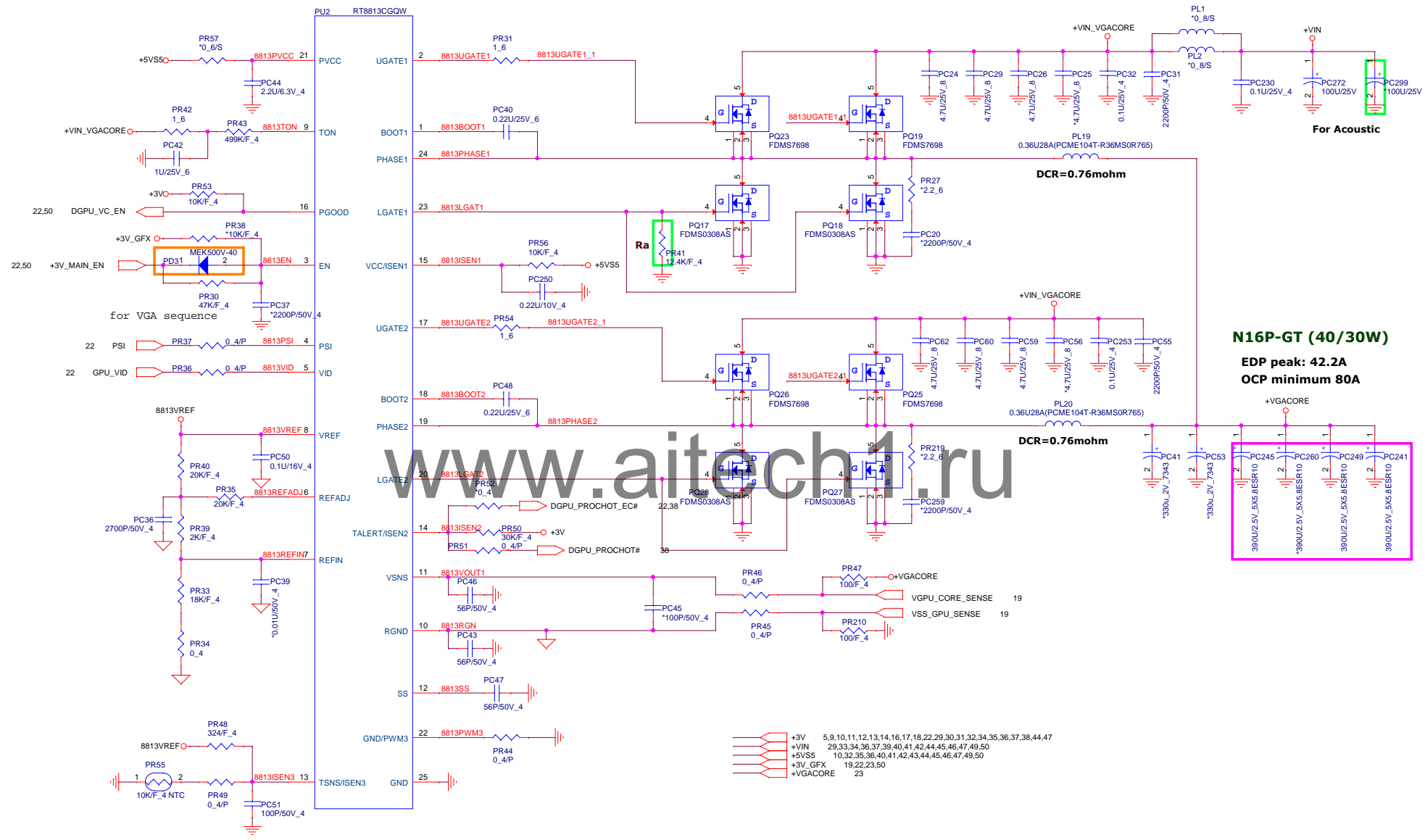


+3V	5,9,10,11,12,13,14,16,17,18,22,29,30,31,32,34,35,36,37,38,44,48
+5V	29,30,31,32,34,35,36,37
+3VS5	10,12,14,16,35,37,38,40,43,50
+5VS5	10,32,35,36,40,41,42,43,44,45,46,48,49,50
+3VSUS	34,36
+3VLAVCC	32,35
+5V_CAM	31
+VIN	29,33,34,36,37,39,40,41,42,44,45,46,48,49,50



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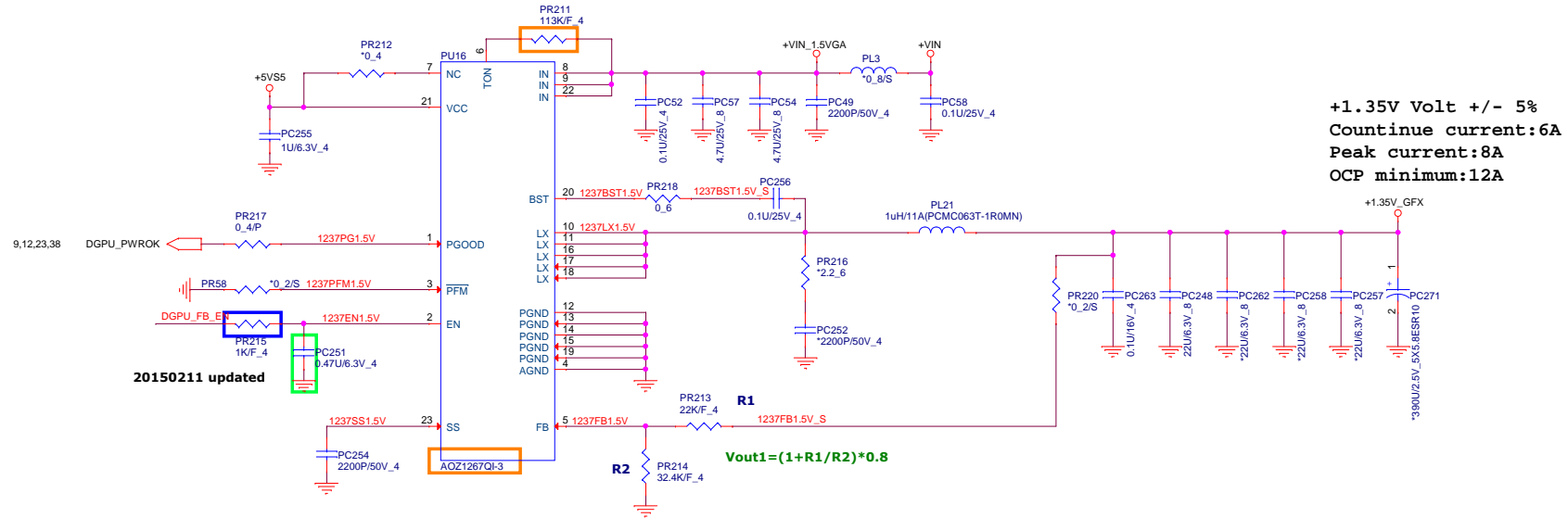




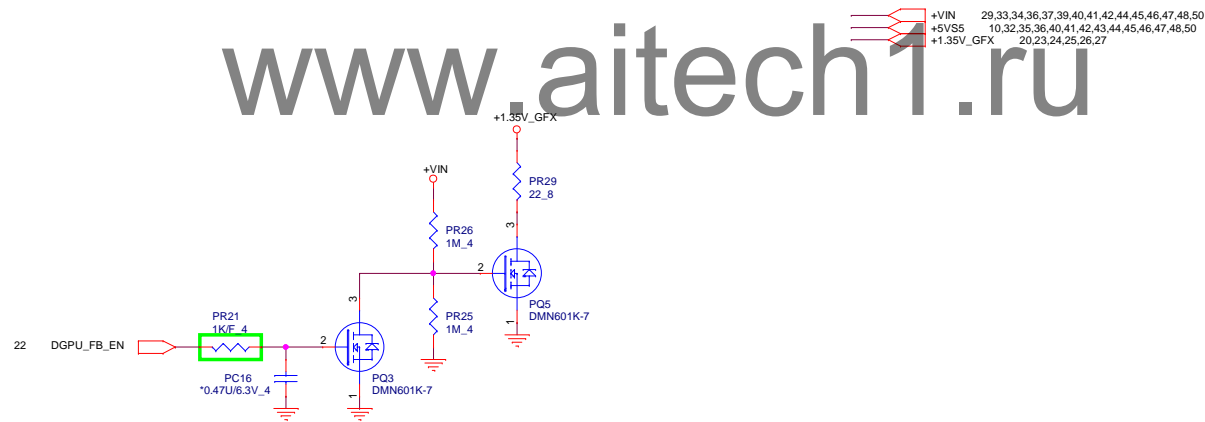
N16P-GT (40/30W)
EDP peak: 42.2A
OCP minimum 80A

- +3V 5,9,10,11,12,13,14,16,17,18,22,29,30,31,32,34,35,36,37,38,44,47
- +VIN 29,33,34,36,37,39,40,41,42,44,45,46,47,49,50
- +5VSS 10,32,35,36,40,41,42,43,44,45,46,47,49,50
- +3V_GFX 19,22,23,50
- +VGACORE 23

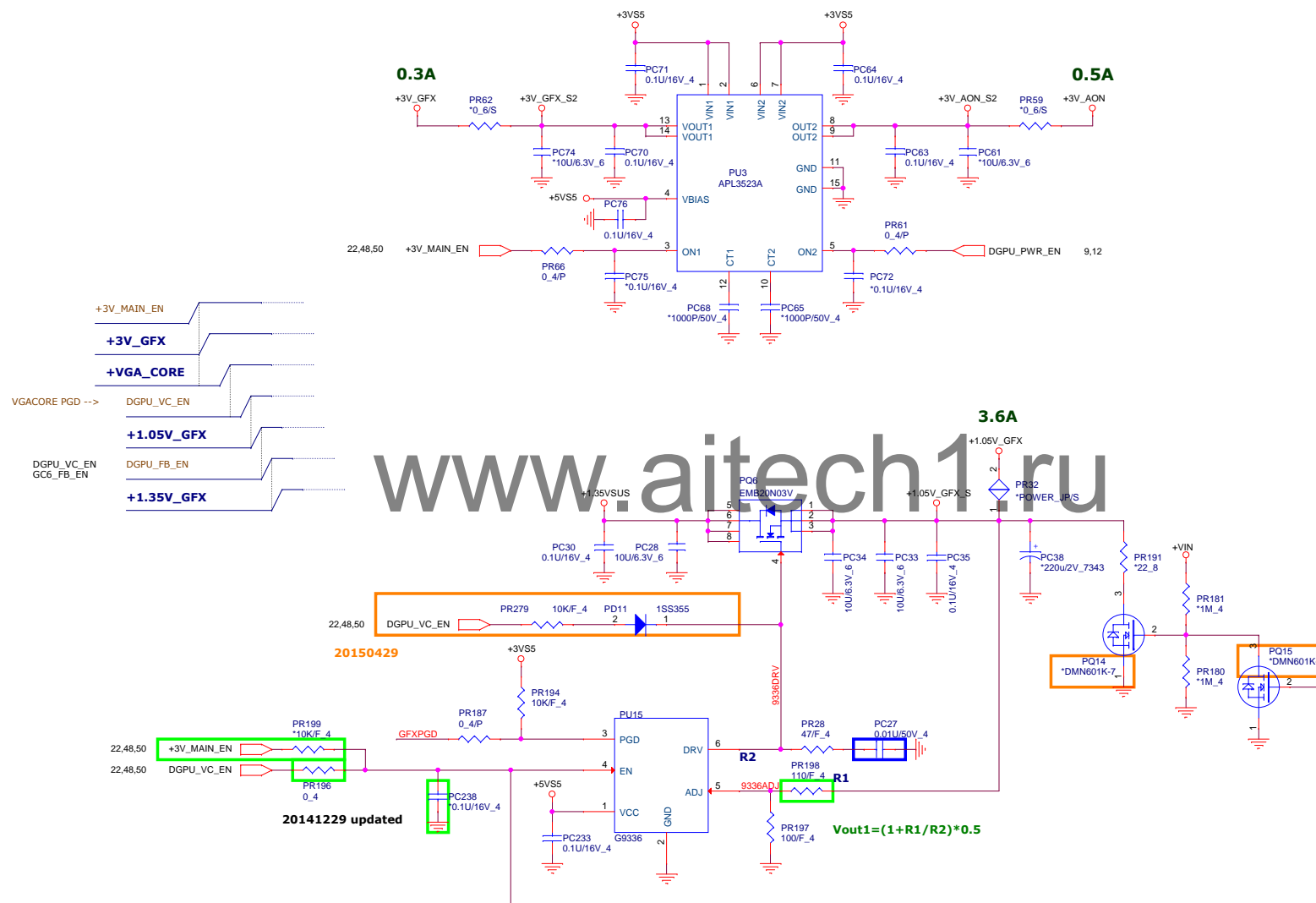
2015/5/6 updated

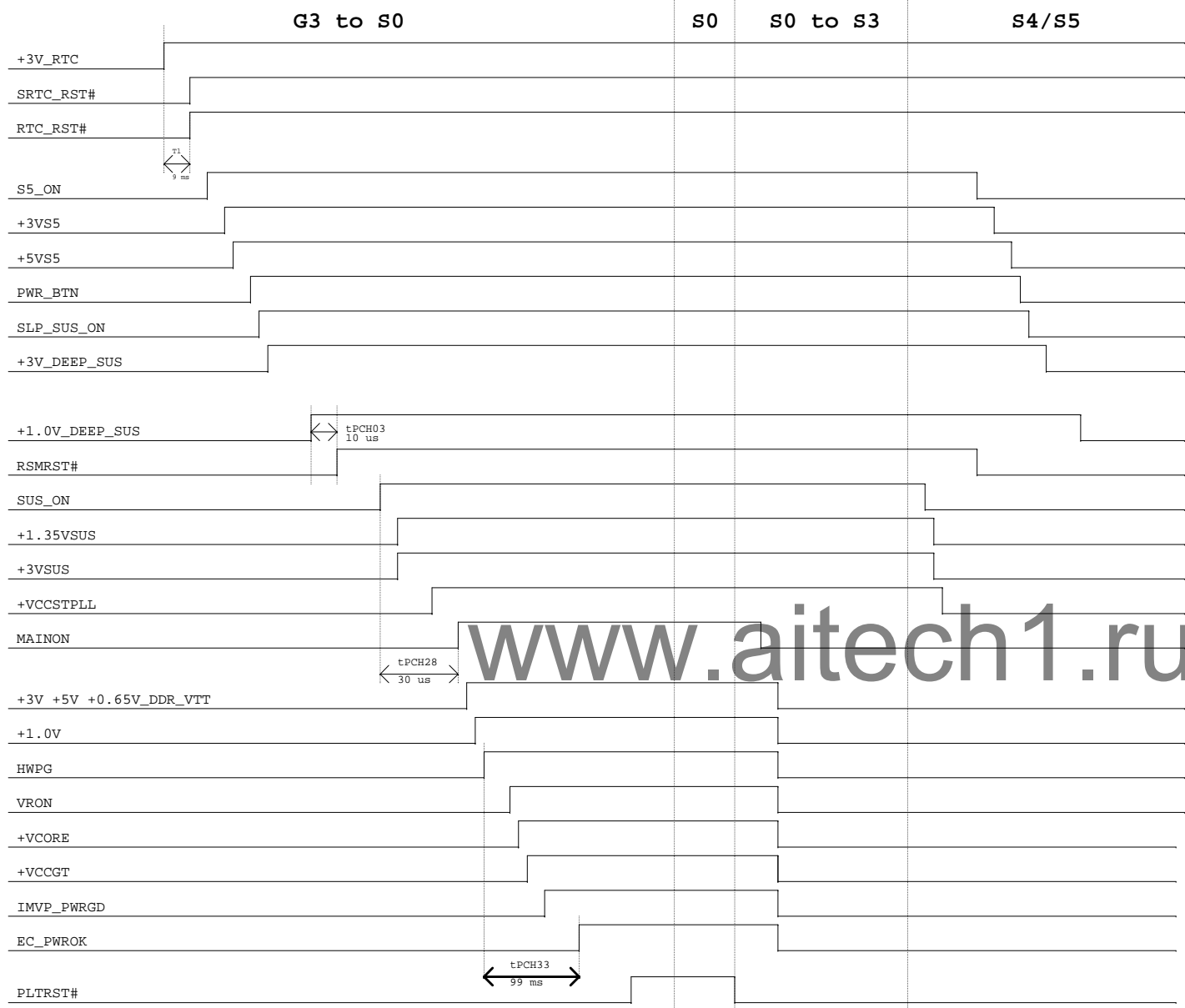


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+VIN	29,33,34,36,37,39,40,41,42,44,45,46,47,48,49
+3VS5	10,12,14,16,35,37,38,40,43,47
+5VS5	10,32,35,36,40,41,42,43,44,45,46,47,48,49
+3V_GFX	19,22,23,48
+3V_AON	19,22,23,35
+1.35VSUS	2,6,10,17,18,41,43
+1.05V_GFX	19,20,21,23





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